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## **THESIS**

CONTROL VANE GUIDANCE FOR A DUCTED-FAN UNMANNED AIR VEHICLE

by

Patrick J. Moran

June, 1993

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## CONTROL VANE GUIDANCE FOR A DUCTED-FAN UNMANNED AIR VEHICLE

by

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Submitted in partial fulfillment of the requirements for the degree of

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#### **ABSTRACT**

Control of airborne vehicles was originally conceived to be done entirely by human pilots. Improvements in electronics in the last 50 years have allowed many flight control functions to become automated, with the pilot continuously monitoring flight parameters from within the vehicle cockpit. With the advent of small unmanned air vehicles (UAV's), which are limited in size and weight-carrying capacity, a pilot is now able to fly an airborne vehicle from a distant ground-fixed position. Miniature electronic instruments control or direct vehicle movements either through pilot commands or autonomously. In order to accomplish reliable, continuous control of a UAV, many sensors are necessary aboard the vehicle. This thesis designed and installed necessary hardware and developed software to guide a UAV's aerodynamic control vanes, with feedback from sensors aboard the vehicle, in order to facilitate ground-based pilot control. Previous thesis work accomplished on this project achieved control of a UAV, named Archytas, in one degree-of-freedom, roll, while mounted on a test stand. Umbilical-controlled guidance of Archytas' control vanes from a forward-mounted sensor pod was set as the goal for this phase of the project. This work focused on modification of hardware to generate and access required signals, programming of analog-to-digital (A/D) and counter/timer peripheral boards mounted in a personal computer to control electrical and signal flow, and implementation of single-input-single-output (SISO) Accesion For

control equations developed concurrently in another thesis

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#### I. INTRODUCTION

Unmanned air vehicles (UAV's) can offer many benefits to potential users, such as relatively low procurement cost, low cost per flight hour, portability, and simplicity of operation. UAV's are also known as remotely piloted vehicles or RPV's. The term 'UAV' is preferred, denoting an air vehicle often flown autonomously. When studied in an academic environment, such as the Naval Postgraduate School's (NPS) UAV Flight Research Laboratory, UAV's offer students and faculty the ability to safely and inexpensively flight test different aircraft designs, configurations, or technologies. Vehicles can be equipped with radio-frequency (RF) uplinks, allowing thorough command of flight conditions. Additionally, UAV's can be instrumented to send data via RF downlinks so that flight parameters can be observed real-time during flight, or recorded for later analysis. Unique or novel designs can be developed to serve special purposes or missions, which may not be achievable or economically feasible using full-scale piloted aircraft. Safety is another benefit of UAV flight research, as personnel are at minimal risk while a vehicle is airborne during flight testing.

In order to accomplish safe, reliable, and continuous control of UAV platforms, a stability augmentation system (SAS) is often required. A SAS consists of motion sensors, an inertial measurement unit (IMU), a filtering scheme such as Kalman filtering, a navigation system such as GPS (global positioning system), and a microprocessor to perform calculations, all mounted aboard the UAV. The SAS sends command outputs

to the vehicle's aerodynamic control surfaces, which then guide the vehicle's trajectory, either through pilot inputs or resonomously. [Ref. 1]

This thesis detail the design and installation of a suite of sensors which provide necessary feedback signals to support the SAS of the prototype Archytas UAV in a hover in the same of the Archytas project will add additional SAS elements to achieve more complicated flight conditions. Software algorithms which implement the control laws to guide the vehicle's aerodynamic control vanes were developed. Algorithms were encoded using the 'C' programming language. An external personal computer (PC), connected to Archytas through an umbilical cord, served as the onboard microprocessor to test the algorithms. Commands were sent to the control vanes either from two joysticks on a control station via umbilical, which represented the RF transmitter, or as a result of control algorithm feedback from the SAS. The algorithms and control system were tested by simulating various inputs, either joystick or sensor generated, and observing appropriate vane responses.

This project continued work begun by Merz [Ref. 2] and Davis [Ref. 3], which implemented a one degree-of-freedom SAS and core control system for the aerodynamic control vanes. Several concurrent student theses in progress will continue to develop other aspects of the SAS, including the IMU, GPS, MIMO controller and Kalman filters, so that the combined theses will contribute to the final design of the Archytas SAS.

This investigation examined:

• The ground control equipment necessary for umbilical-controlled flight of the Archytas UAV including the PC's required for generation of control signals, the

control junction board on which two joysticks and associated control switches were mounted, and the umbilical cord.

- The redesign of on-board vehicle hardware, including reconfiguration of the previously-designed sensor pod from the vehicle's rearbody to a forebody mounted on struts above the vehicle intake. All wiring harnesses and associated bus boards were connected to accommodate the redesign.
- The modification of 'C'-coded software to generate pulse-width modulated (PWM) signals through programming of counter/timer and analog-to-digital (A/D) boards mounted within the PC's. The PWM signals were used to drive servos which in turn positioned the UAV's four control vanes and throttle.
- The implementation of a single-input-single-output (SISO) and multiple-input-multiple-output (MIMO) controllers as progressive steps toward achieving hovering flight of the Archytas UAV.

This thesis consists of six chapters, including this introduction. Chapter II summarizes evolution of the Archytas project. Chapter III details the ground equipment required for this phase of the project. Chapter IV describes the vehicle's on-board equipment and systems, such as sensors, servos, and signal distribution. Chapter V concerns implementation of control algorithms which develop pulse-width modulated signals to drive the vehicle's control vanes. Finally, Chapter VI deals with system evaluation, results achieved, conclusions derived, and recommendations for future improvements to the system. Appendix A contains 'C' routine listings developed for the project. Appendix B includes plug diagrams, and sensor and peripheral hardware specifications. Appendix C contains schematic diagrams used extensively in the vehicle's signal and electrical connections. Finally, Appendix D is an engine-run safety checklist developed to ensure that proper safety precautions were adhered to during engine runs.

#### II. BACKGROUND

This chapter begins with a summary of Close-range UAV requirements, and follows with a development of the history of the Archytas project to date. Archytas UAV project mission and goals are also described.

## A. GENERAL INFORMATION ON UAV'S

The DoD Unmanned Aerial Vehicles Joint Project Office (UAV JP), which oversees military unmanned air vehicle programs, classifies UAV's into four categories based on a vehicle's intended operating range or endurance:

• Close-range: 3

30-50 km range,

• Short-range:

150 km range,

Medium-range:

650 km range,

• Endurance:

up to 24 hour flight time.

Close-range UAV's tend to be small, portable vehicles with minimal support equipment. Short-range UAV's are generally larger, requiring incrementally more support equipment and personnel for operation. Medium-range UAV's rival manned aircraft in size, speed, and endurance, and are similar to non-lethal cruise missiles. Endurance UAV's tend to be large in size with glider-like wingspans, and have a desired endurance on the order of days. [Ref. 4]

The Archytas project is directed toward the Close-range UAV category. A Close-range UAV would ideally be employed at Marine Corps expeditionary unit (MEU) or Army battalion level for battlefield intelligence surveillance. Historically, UAV designs require prepared runway surfaces on which to conduct flight operations. This requirement has tended to limit UAV usefulness in battlefield scenarios, which often do not allow the luxury of a dedicated, prepared runway. Previous designers have overcome this shortcoming by using pneumatic launchers or rocket-assisted takeoffs (RATO), but such methods require numerous personnel and bulky support equipment, severely restricting overall UAV system portability. To that end, the UAV JP Master Plan for 1992 included a requirement for a VTOL (Vertical Take-off and Landing) UAV system, which must be able to operate from U.S. Navy surface combatants. VTOL UAV specifications call for a 135 kt cruise speed, 110 nm radius of action, 3 hour loiter time at 110 nm, and 12,000 ft operating altitude. [Ref. 5, pp. 34-6]

A configuration which could meet UAV JP requirements is a hybrid VTOL, which would take-off vertically, using very little ground/deck space. Once airborne the vehicle would transition to horizontal flight to take advantage of the improvements in speed, range, and loiter time which horizontal fixed-wing flight provide. [Ref. 6]

The Archytas UAV is an NPS UAV Flight Research Laboratory project currently under development. The ducted-fan, tail-sitter configuration of Archytas is a VTOL platform being investigated to explore technologies related to the VTOL UAV mission. Archytas' namesake was a citizen of ancient Greece and colleague of Plato credited with having designed, constructed, and flown the first mechanical bird. [Ref. 6]

The U.S. Coast Guard is closely following the development of prototype vehicles which are designed to meet the VTOL UAV specifications. The potential benefits from UAV technology for the Coast Guard's law enforcement efforts are numerous. UAV's could be operated from strategic island locations throughout the Caribbean much more efficiently than costly manned aircraft. Also, Coast Guard ships are similar in size to the small surface combatants specified in the VTOL UAV specifications. As the Coast Guard's research and development budget is far less than DoD's, any technologies advanced by DoD research facilities, such as the Naval Postgraduate School, are frequently later employed by the Coast Guard.

### B. ARCHYTAS CONCEPT EVOLUTION

The Archytas UAV is mainly built from the assets of two previously cancelled DoD developmental UAV programs, the U.S. Marine Corps' AROD program and the U.S. Army's Aquila program, both of which are described below. The Archytas mission requires a lightweight, inexpensive UAV which, due to its VTOL capability, can be operated from very confined locations, including flight decks of ships at sea. In its current configuration, Archytas is not specifically designed to meet VTOL UAV requirements. But as a VTOL technology demonstrator, it has the potential to validate the concept of a ducted-fan, tail-sitter-configured UAV, and to design, test, and validate sensor and software requirements for guidance, navigation, and control of such a platform.

Previous work in the NPS UAV Flight Research Lab achieved design and initial testing of a preliminary half-scale version of the Archytas concept. This work introduced the ducted-fan-in-fuselage configuration, although the original concept had been to keep the wings horizontal during hover, rotating only the duct and engine to transition from hover to forward flight [Ref. 7]. The new Archytas configuration, a tail-sitter design with a rigidly-mounted duct, was chosen due to the availability of useful assets and its design simplicity. The Archytas UAV's current configuration is shown in a three-view sketch with approximate dimensions in Figure 1.

Assets remaining from the cancelled AROD and Aquila programs have been acquired by NPS. Elements of each design are being used to further explore the ducted-fan-in-fuselage, tail-sitter UAV configuration. Positive aspects of each design are combined in the Archytas design to form a more efficient, more capable platform than either AROD or Aquila. The following sections describe each vehicle, then how they are combined to form the Archytas UAV.

#### 1. AROD

The U.S. Marine Corps identified a requirement in the late-1970's for an MEU-level intelligence gathering tool, and so commissioned the development of a Close-range UAV to fill that need. Program development was engineered by Sandia National Laboratory at Albuquerque, New Mexico, in conjunction with the Naval Ocean System Command, in the mid-1980's. The result of their design efforts was the Airborne Remotely Operated Device, or AROD. AROD was built to fly strictly in a hover or vertical flight mode. While filling the requirement for operation from unprepared areas,

## Dimensions Approximate

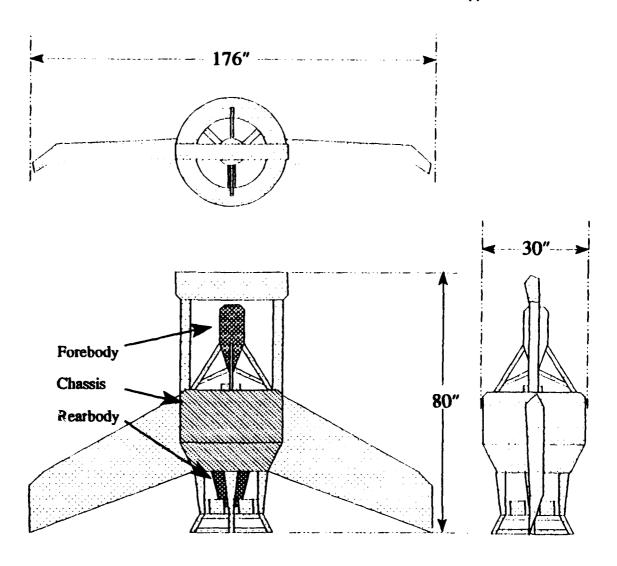
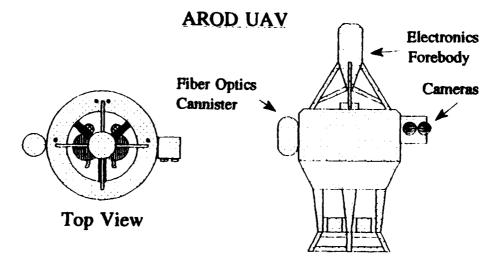


Figure 1 Three-view Sketch of Archytas UAV General Configuration

the configuration was lacking in that its vertical flight mode was inherently inefficient for transitional flight. Its ducted-propeller design did offer the advantage of safety when operated in close proximity to ground personnel, as well as high thrust efficiency. AROD was intended to be controlled primarily through a fiber-optic (FO) link, with RF control available for backup and as a training aide [Ref. 8]. It first flew successfully in 1986, but never went into production. One problem the AROD design suffered from severe vibration. Also, an updated requirement by the newly-formed UAV JP called for the UAV to be able to achieve translational flight, rather than simply hovering flight, which AROD was not designed to do. The AROD project was cancelled in 1987 due to budget cutbacks [Ref. 6]. Equipment assets remaining from the AROD program were acquired by NPS in 1992. Those assets provide much of the hardware which now makes up Archytas' fuselage and electronics. A two-view sketch of AROD is shown in Figure 2.

## 2. Aquila

Prior to the AROD program, the U.S. Army developed a short-range UAV, named Aquila, which also never saw operational employment. A pure fixed-wing design engineered by the Lockheed Corporation, Aquila was launched from a pneumatic launch device mounted on a large truck. Aquila was a delta-wing configured RPV, with a wingspan of 12 feet 9 inches. It was powered by a 2-stroke, 2-cylinder gasoline engine, which drove a pusher propeller. Figure 2 shows the general Aquila configuration in a three-view sketch. The Aquila engine, a Dyad 200 made by Herbrandson Engines, Inc., was derived from a commercial chainsaw engine. Coincidentally, the same engine was



Profile View, Symmetrical

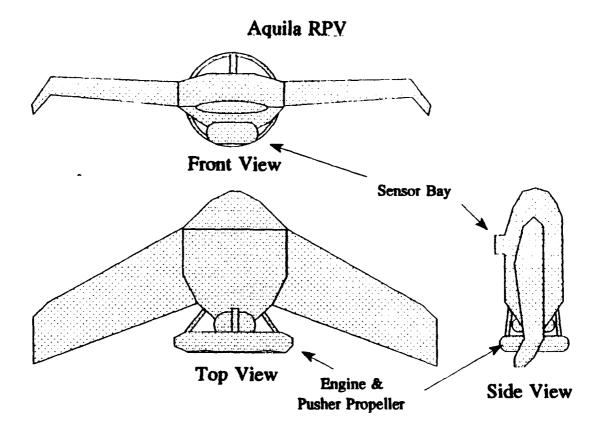


Figure 2 Two-view sketch of AROD UAV and three-view sketch of Aquila RPV

later chosen by Sandia Laboratory for AROD [Ref. 4]. Like the AROD project, the Aquila project was cancelled due to budget cutbacks. The wings from Aquila, which were reflexed to provide longitudinal stability for Aquila's delta-wing shape, are used in the Archytas design by adapting them to mount on an AROD fuselage [Ref. 6]. Figure 2 also shows a three-view sketch of the Aquila RPV.

## 3. The Archytas UAV

Archytas project goal is to couple the hovering takeoff and landing benefits of AROD with the inherently more efficient and faster horizontal flight characteristics of Aquila. Archytas was designed in a tail-sitter configuration with the AROD body forming the Archytas fuselage and Aquila's wings providing horizontal flight lift. A forward canard was added to provide longitudinal stability in horizontal flight.

AROD's electronics were originally mounted around the exterior shell of its chassis. They were relocated to a pod mounted above its intake duct for center-of-gravity (CG) and vibration considerations. In the initial stages of the Archytas design, the pod was moved aft of the engine and control vanes so as not to impede intake airflow and to ease implementation of the electronics suite [Ref. 2]. Later, longitudinal stability calculations determined that Archytas, like AROD, needed to have the pod forward for CG considerations, despite the potential airflow hinderance [Ref. 9]. Also, experience with the rearbody-mounted electronics pod during engine runs revealed that severe vibration generated by the vehicle's 2-cycle engine was being transferred to the pod. The vibration led to numerous component failures, which served to reinforce the decision to move the electronics pod to the forebody location.

Archytas uses the Aquila and AROD 26-horsepower engine for propulsion. The engine, coupled with a three-bladed ducted propeller, provides approximately 120 lbs of thrust to propel Archytas. To accomplish vertical take-off and landing, only 25 lbs of excess thrust are available. This limits the amount of equipment and payload the vehicle can carry. All of AROD's original fiber optic control equipment and surveillance systems were stripped from its fuselage, which then became the Archytas fuselage. Only the minimum equipment necessary for prototyping the concept was retained. A minimal fuel system is employed, again only for validation of the concept.

Future goals of the Archytas project are to optimize the tail-sitter VTOL configuration by adding state-of-the-art sensors, GPS receivers, and on-board microprocessors, allowing for fully autonomous flight guided through a MIMO controller. Uplinks and downlinks will provide continuous vehicle command and monitoring of flight parameters.

A general block diagram of the Archytas UAV control system developed in this thesis is shown in Figure 3. The following two chapters will detail the ground control equipment and on-board vehicle equipment shown in the diagram.

Chapter V then discusses the control vane guidance algorithms written to control the system. In general, the system works through sensors aboard the Archytas which sense vehicle motions and output analog signals. The sensor signals are conditioned aboard the vehicle to the proper voltage ranges, then are sent to the PC via the umbilical cord and control junction board. This information is used by a pilot, or a control algorithm, to guide the vehicle. Pilot inputs from two joysticks mounted on the

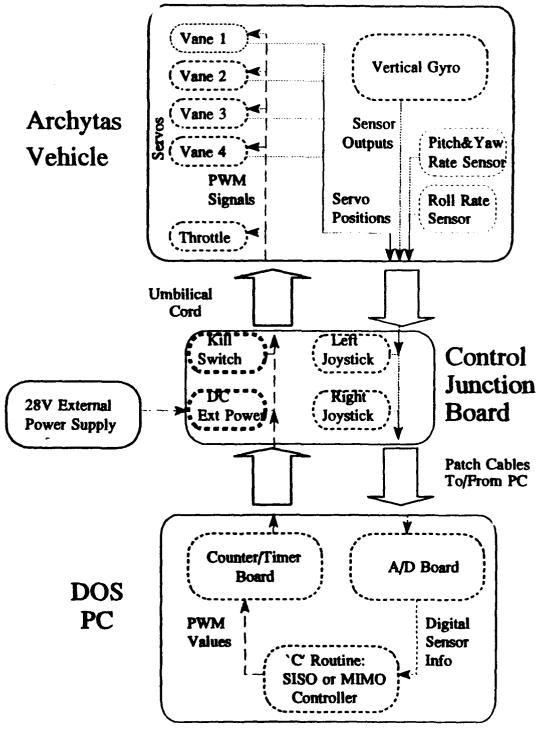


Figure 3 General Block Diagram of the Archytas UAV Control System

control junction board are converted from analog signals to digital numbers and are sent to the counter/timer boards in the PC, possibly via a control algorithm, to generate pulses. The pulses vary in width proportionally to the desired position of the servos. These pulses are then sent back to the vehicle via the umbilical cord to guide servos attached to the vehicle's control surfaces and throttle.

## III. GROUND CONTROL EQUIPMENT

The ground equipment necessary to control Archytas in this phase of development is described in this chapter, as depicted in Figure 4. Much of the equipment was unique to this phase of the project, and will either be removed or relocated as the Archytas project progresses. For instance, the external PC which controls generation of pulsewidth modulated (PWM) signals will be replaced in a future developmental phase by a 486 CPU aboard the vehicle to perform the same function. When this occurs, many umbilical cord functions will no longer be necessary. Commands will be passed between the pilot and vehicle by data link. It should be evident that many other changes in configuration will occur as the project progresses.

As described in Chapter I, the Archytas UAV will eventually have a self-contained stability augmentation system (SAS) aboard the vehicle centered around a micro-processor. To ease implementation and design of the SAS, it was felt that development should begin with an umbilical cord control system, with the micro-processor role being accomplished by an external PC. This would allow for developing the RF data link at the same time that the control system was being developed. A method of passing information to the PC via the umbilical cord, then transmitting commands back to the vehicle, was developed independently at this stage of design. Equipment necessary to accomplish this process included the PC's, the associated counter/timer and analog-to-digital boards, the control junction board which is the mounting location for two joysticks

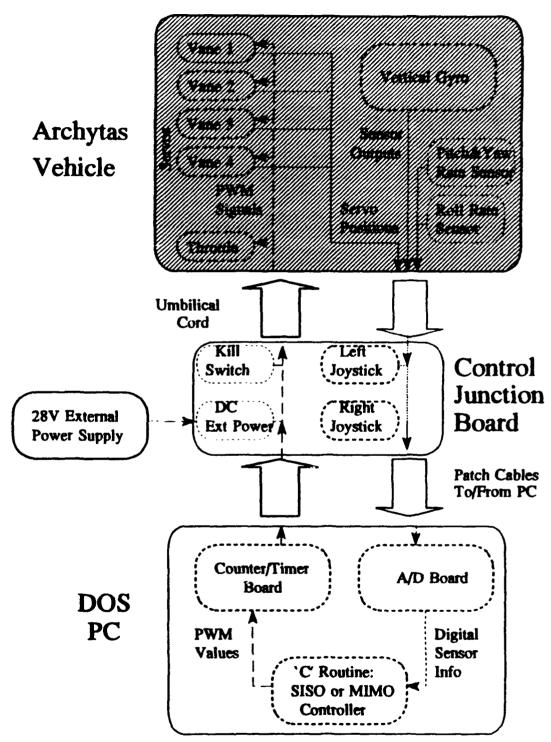


Figure 4 Archytas UAV Ground Equipment Block Diagram

and associated switches, and the umbilical itself. This equipment is described in this chapter.

#### A. COMPUTERS

The NPS UAV Lab operates two IBM-compatible PC's, a RAMPCOM 486DX 50 MHz and an IBM AT retrofitted with an OPTi 386DX 25 MHz motherboard. Both were used in development of 'C' routines written for Archytas using a Borland 'C' compiler. The 'C' programming language was used by Merz [Ref. 2] to develop initial PWM and A/D routines because it is relatively user-friendly and is widely used in engineering applications. Later phases of development necessarily follow the same 'C' conventions. Two peripheral boards, described in the next section, were used in each computer to control signals coming to and from the PC's through 'C' routines.

## B. PC PERIPHERAL BOARDS

## 1. Counter/timers: 'Quartz' and 'CIO-CTR'

In order to develop PWM signals, a method of regulating the duration of each pulse was required. Two counter/timer boards, 'Quartz' made by Diamond Systems Corporation and 'CIO-CTR' made by Computerboards Inc., were used to generate PWM signals to control the aerodynamic control vane servos and the throttle servo. These boards were programmed to regulate pulses to a desired width and repetition rate. Specific register-level programming instructions for each board, as well as the A/D boards described in the following section, were well-documented by Merz [Ref. 2,

Chapters IV and V]. Both boards employ an Am 9513A counter/timer microchip as the heart of their circuitry. This compatibility allowed 'C' routines to be run interchangeably through either board in either computer. The Am 9513A chip has five individual counters, as well as a user-selectable frequency oscillator. The CIO-CTR board was configured with two Am 9513A chips, allowing the use of 10 counters, although this feature was not used since only five counters were necessary. The only difference operationally between the boards was their different connector plugs: Quartz used a 50-pin plug, while CIO-CTR used a 37-pin connector. This required the control junction board, which connected PC cables to the umbilical, to have adapters for both plug arrangements. The pin-out diagrams and necessary connections for each counter/timer are shown in Appendix B. Initial DIP switch settings for each board, which define the board's operating modes, are defined in Table 1.

TABLE 1: COUNTER/TIMER BOARD DIP SWITCH SETTINGS							
Board	Base Address	Interrupt	Wait State				
Quartz	220 Hex	5	N/A				
CIO-CTR	220 Hex	X (No IRQ)	On				

## 2. A/D Boards: 'CIO-AD16jr' and 'DAS-16'

An A/D board converts analog voltage signals to digital numbers, which can then be manipulated by software and the counter/timer boards to produce PWM signals. The A/D boards used were the 'CIO-AD16jr' made by Computerboards Inc., and 'DAS-16' made by the Metrabyte Corporation. Like the counter/timer boards, both A/D boards employed compatible microprocessors and timers, the Intel 8254 programmable interval timer, to complete the A/D conversion process. Again, the boards' compatibility allowed for 'C' routines to be run interchangeably on either computer. Both A/D boards used similar 37 pin plugs, but different pin arrangements. Fortunately, the pins used for Archytas were the same on both plug arrangements, easing interoperability. The pin-out diagrams for each A/D board and the necessary connections are shown in Appendix B. The initial DIP switch settings for each A/D board are shown in Table 2.

TABLE 2: A/D BOARD DIP SWITCH SETTINGS							
Board	Base Address	Channel	Clock	<u>DMA</u>	Input Range		
CIO-AD16jr	300 Hex	16	1	1	Software-Driven		
DAS-16	300 Hex	16	N/A	1	11000		

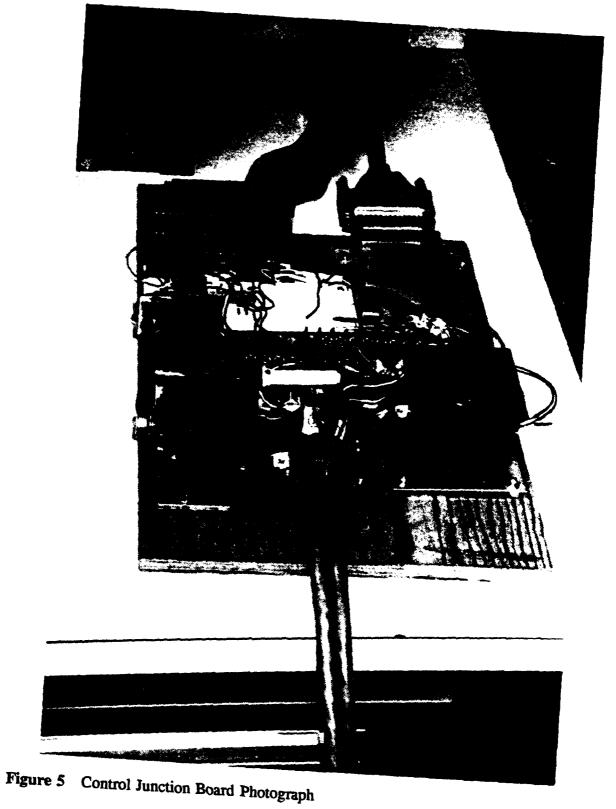
## C. CONTROL JUNCTION BOARD AND UMBILICAL CORD

Commands which guide Archytas' control vanes and throttle were made using two joysticks mounted on a control junction board. Earlier generation routines by Merz [Ref. 2] used input from only one joystick. The joysticks were arranged in a layout similar to that of a standard radio-control (RC) transmitter. The left joystick, taken from a surplus Futaba transmitter, controlled inputs to the rudder (left/right, spring-centered) and throttle (up/down, not spring-centered). The right joystick, taken from a surplus AROD ground control station, commanded ailerons (left/right, spring-centered) and elevator (up/down, spring-centered).

A photograph of the control junction board in its current configuration is shown in Figure 5. It shows the dual joysticks, umbilical cord connections, PC patch cables, kill switch, and DC external power connections.

A remote kill switch, which was a direct tap into the engine's ignition wire through the umbilical cord, was also mounted on the control junction board. The entire control junction board will eventually be replaced by an actual RC transmitter in a later project phase when the umbilical is no longer required.

Each joystick single degree-of-freedom had a different voltage range from its lower limit to upper limit. The left stick also was equipped with trim knobs for each degree-of-freedom. To prevent inadvertent movement of the trim knobs, the knobs were taped in the full down position for throttle, and centered position for rudder to ensure consistent voltage outputs. The analog voltage difference from each joystick output, when passed through the A/D board, translated to a range of digital values. The A/D board used a



12-bit conversion, so that a 0 to 5 volt analog range translated to a 0 to 4096 digital range. Table 3 lists the minimum, center, and maximum digital units for each stick's two degrees-of-freedom. These values were recorded empirically.

TABLE 3: JOYSTICK DIGITAL UNIT RANGES							
(AFTER A/D CONVERSION)							
<u>Joystick</u>	Joystick Minimum Center Maximum Minimum Center Maximum						
!	(Down)		(Up)	(Left)		(Right)	
Left	2490	N/A	3140	1600	1150	720	
Right	1850	2260	2650	2470	2090	1680	

The umbilical cord used consisted of shielded six-twisted-pair cable, obtained from surplus telephone cable. Since 21 signals needed to be transferred from the ground station to/from the vehicle, two cables were used side-by-side. The two cables were labelled '1' and '2' and tie-wrapped together for security. Signal flow through the umbilical is charted in Table 4. The vehicle's umbilical connection was at the lowest point of the rearbody. The earlicap which would have normally been located at the bottom of the rearbody was replaced by a surplus forebody topcap. This arrangement allowed an easier and more secure method of mounting the umbilical cord to the vehicle. The Cannon plug connections at the vehicle end and wiring connections at the control junction board end of the umbilical cord are shown in Appendix B.

## TABLE 4. UMBILICAL CORD SIGNAL FLOW Total Wires: 21, plus shield & chassis ground. 23 of 24 Umbilical Pins Used. To Vehicle fm Control Junction Board Fm Vehicle to Control Junction Board 4 Vane Command Signals: 3 Rate Signals: Roll, Pitch & Yaw Rate Vanes 1, 2, 3, & 4 3 Control Positions: Aileron, Elevator & Rudder Position 1 Throttle Command Signal 2 Kill Switch Leads to Ignition 2 Vertical Angles: Pitch & Yaw External Power: 1 Tachometer Signal 28V DC & Reference (2 of each) 1 Common

The control junction board also provided a convenient mounting location for external 28V DC power connections. External DC power was passed through the umbilical cord for use whenever the vehicle's engine-driven alternator was not supplying power. This also allowed operation of the vehicle engine with external electrical power supplying the vehicle. The alternator could then be disconnected for troubleshooting electrical system problems.

In summary, the ground control equipment described in this chapter includes:

- PC to perform micro-processor calculations via 'C' programming;
- Counter/timer board mounted within the PC to generate PWM signals;
- A/D board mounted within the PC to convert analog voltage signals from sensors and joysticks to digital values for PWM generation;

• Control junction board to serve as a connection point between the umbilical cord and PC counter/timer and A/D patch cables, and as a mounting location for two pilot joysticks and a remote kill switch.

In a later phase of the Archytas project, the external PC will be replaced by a 486 CPU aboard the vehicle. This will occur after all umbilical cord testing has been successfully accomplished and an RF data link has been developed. The control junction board's joysticks will be replaced by joysticks on an RC transmitter. The remote kill switch will be operated through a dedicated servo on the vehicle commanded from a switch on the RC transmitter. A receiver and antenna will be mounted on the vehicle to complete the RF data link for control and flight parameter information transmission.

## IV. ON-BOARD VEHICLE EQUIPMENT

The Archytas UAV fuselage, as depicted in Figure 6, can be divided into three sections: the forebody, located on thin aluminum struts above the intake; the chassis, which is the main Archytas body made from the AROD fuselage; and the rearbody, which is below and inside of the chassis. Equipment related to this thesis was located in or on all three sections. A schematic diagram drawn for the AROD project was used extensively for forebody/chassis/rearbody wiring connections. The schematic is included in Appendix C in two diagrams titled "AROD-3 SCHEMATIC" and "SHEET 2 OF 2." Several full-size blueprint copies of the schematic are available in the NPS UAV Lab for closer inspection. Schematic diagrams of the Archytas control system are also included in Appendix C. A photograph of the Archytas forebody/chassis/rearbody assembly is shown in Figure 7.

#### A. FOREBODY

The forebody is a pod mounted on tubular aluminum struts above the engine intake. It houses the SAS sensors, signal conditioning circuit boards, and DC power distribution boards. A 28V fan mounted in the forward end, or top, circulates air within the pod for cooling. Electrical connections to the forebody are made through Cannon plugs located adjacent to the four strut mounting points. Connecting wire harnesses are guided down each strut and around the outside of the intake to the chassis. Individual components

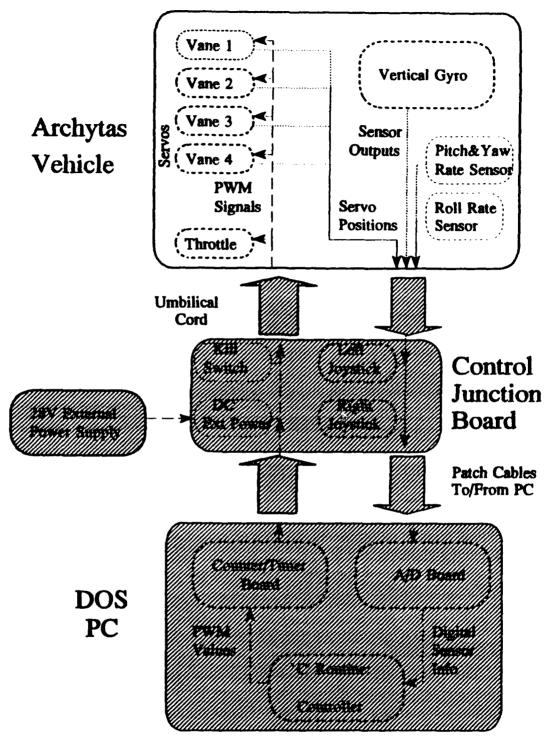


Figure 6 Archytas UAV On-Board Equipment Block Diagram



Figure 7 Photograph showing Archytas Forebody/Chassis/Rearbody Assembly

within the forebody are detailed in the following sections. A photograph depicting the sensors mounted in the lower portion of the forebody is shown in Figure 8.

# 1. SAS Sensors

The flight parameters which needed to be monitored and fed back for the a SAS to operate in a vertical hover flight mode were determined to be angular rotation rates in roll, pitch and yaw about each body axis x, y, and z, respectively, and vertical pitch and yaw angles about the y and z body axes, respectively [Ref. 1]. The sensors necessary to measure these parameters are three angular rate sensors and a vertical gyroscope.

Sandia Laboratory designers referenced AROD parameters using strut three as the 'top' of AROD. Strut three can be quickly distinguished from the other three struts by two small fuel tank vents located atop the intake next to the forebody connecting rod mounting point. There is one vent at struts two and four, and no vents at strut one. This convention led to labeling vane three, located below strut three, the 'upper rudder'. Following the same convention, vane one is the 'lower rudder', vane four is the 'left elevator', and vane two is the 'right elevator'. The natural body axes are then: x aligned with AROD's vertical axis, with positive being forward, y positive toward strut two, and z positive toward strut one. [Ref. 8]

The same axes used for AROD were chosen for the Archytas UAV. This choice specified the attachment points for the UAV's left and right wings. Angular rotation rates and angle measurements for Archytas are then: positive roll angle and rotation rate clockwise about the vertical x axis; positive pitch angle and rotation rate



Figure 8 Photograph of SAS Sensors mounted in Forebody

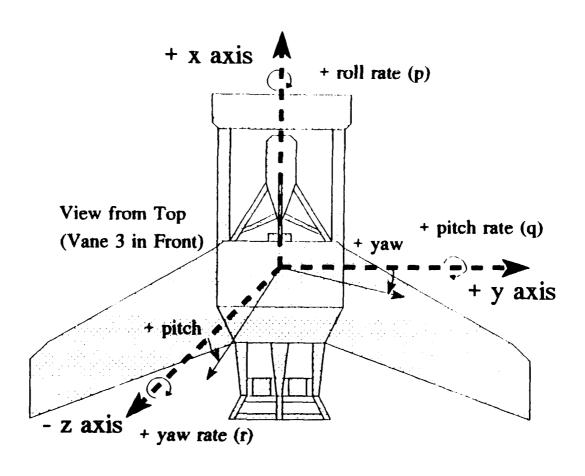
toward strut three and about the y axis; and positive yaw angle and rotation rate toward strut two and about the z axis. Figure 9 graphically depicts the axis and vane numbering conventions applied to Archytas.

# a. Angular Rate Sensors

The angular rate sensors used aboard Archytas ware Humphrey RT-01 and RT-09 single and dual rate sensors, respectively. Rate sensors are the "solid state functional equivalent of a rate gyroscope. They provide an output voltage that is linearly proportional to the angular rate of the sensor" [Ref. 10]. The RT-01 was used to sense roll rate about the x axis. The RT-09 measured pitch and yaw rates about the y and z axes. Appendix B contains diagrams of the electrical connections for the RT-01 and RT-09 angular rate sensors. Appendix B also contains manufacturer's specifications for both rate sensors, including maximum rates of 100°/sec for roll from the RT-01, and 100°/sec for pitch and yaw from the RT-09. The specifications call for at least one minute of warm-up before accurate rates can be measured. Electrical interconnections for both sensors are shown on the diagram titled "AROD-3 SCHEMATIC" in Appendix C. Forebody plug "P2" connects both rate sensor outputs to the A1 CHANNEL CONDITIONING BOARD.

# b. Vertical Gyroscope

A Humphrey VG-34 vertical gyro was used to measure pitch and yaw angles from vertical. Appendix B includes an electrical connection diagram for the



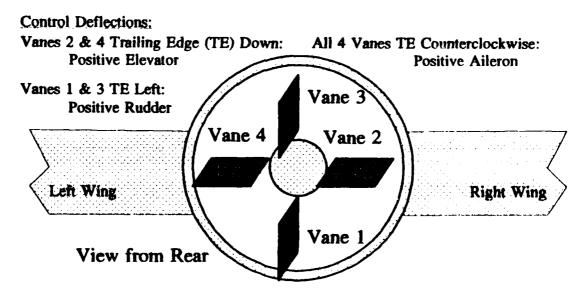


Figure 9 Axis and Vane Numbering Conventions for the Archytas UAV

VG-34. Appendix B also contains manufacturer's specifications for the VG-34, including a maximum angle of  $\pm 60^{\circ}$  in pitch and  $\pm 90^{\circ}$  in yaw, each accurate to  $\pm 1^{\circ}$ . As listed in the specifications sheet, a warm-up time of at least 5 minutes is required before accurate angles will be measured. Electrical interconnections for the vertical gyroscope are shown on the diagram titled "AROD-3 SCHEMATIC" in Appendix C. Forebody plug "P1" connects vertical gyroscope outputs to the A1 CHANNEL CONDITIONING BOARD.

# 2. SAS Sensor Modeling

A mathematical model of Archytas was developed in concurrent thesis work by Kuechenmeister [Ref. 12] in order to predict how the aircraft will actually perform in flight. Creation of the model involved representing each item of the Archytas control system as a dynamic element with its own characteristics within the larger, overall system. The vertical gyroscope and angular rate sensors, which react to vehicle motions, were represented using data derived from their manufacturers' specifications.

# a. Angular Rate Sensors

A rate gyro is a gyroscope which reacts to angular rotational speeds in only one degree-of-freedom. Although the rate sensors used for Archytas were solid state devices, and not actual gyros, the theory of operation is the same. Rate gyros measure angular rotation rates by measuring the linear displacement of a spring attached to the gyro's gimbals, which is a direct measure of input speed. The measurement sensitivity of a given gyro is a function of the stiffness of the gyro's spring. The

spring's displacement is a reaction to torque developed by the momentum force due to the spinning gyroscope. The entire system for one degree-of-freedom can be expressed as a second-order transfer function:

$$V(s) = \left(\frac{\omega_n^2}{s^2 + 2s\zeta\omega_n + \omega_n^2}\right) \cdot (\omega_i(s) - D(s))$$
 (1)

where V(s) is the LaPlace transform of the output voltage,  $\omega_i(s)$  is the LaPlace transform of the input rotation rate, D(s) is the error term, and  $\omega_n$  and  $\zeta$  are the natural frequency and damping ratio of the gyro, respectively [Ref. 11, pp. 9-11]. Manufacturer's specifications in Appendix B for both RT-01 and RT-09 sensors list  $\omega_n$  as 25 Hz minimum and  $\zeta$  as 0.7 typical. Assuming a zero error (D(s)=0), the LaPlace transform of the output voltage for each degree-of-freedom should be:

$$V(s) = (\frac{625}{s^2 + 875s + 625}) \cdot \omega_i(s)$$
 (2)

# b. Vertical Gyroscope

A vertical gyroscope is referenced to gravity in the downward direction. Therefore, it measures two angles in two degrees-of-freedom from vertical. Slaving to gravity is accomplished through closed-loop feedbacks within the gyro. Level sensors are mounted on gimbals, which are in turn coupled to slow-speed erection motors. The motors maintain the gimbals in a level condition in both degrees-of-freedom. In a

conventional aircraft application, each gimbal angle then would give an attitude angle, either pitch or roll, relative to local vertical [Ref. 11, pp. 11-12]. Since the Archytas axes were aligned with 'x' in the up or vertical direction, rather than the typical aircraft convention of 'x' in a forward direction, its vertical gyro measured pitch and yaw angles.

# 3. Forebody Interconnections

In order to bring each SAS sensor output to the standard voltage range of 0-5V, which was required by the A/D board, their outputs were conditioned by the A1 SIGNAL CONDITIONING BOARD and the anti-aliasing FILTER BOARD. The plug pin-outs for connections within the forebody to and from the A1 SIGNAL CONDITIONING BOARD and the FILTER BOARD are shown in Appendix B. Other plugs used in the forebody are described in Section E of this chapter, and listed in Table 5.

#### B. CHASSIS

AROD's main chassis became the central fuselage chassis for Archytas. Equipment attached to the chassis and used for this phase of the project are detailed in following sections. In essence, from an electronics perspective, the chassis served only to provide attachment points for signal and electrical distribution buses. The only active component attached to the chassis was the diode rectifier board which translated alternator-produced AC power into necessary DC power for electronics. The Archytas UAV used no AC power directly.

# 1. Diode Rectifier Board

Electrical power used by Archytas is supplied by either of two sources. The primary source of power when the vehicle's engine is running is an engine-driven alternator, to be described in a later section. Alternatively, an external power connection to a 28V DC power supply is made through the umbilical cord.

The alternator provided 20-50V AC three-phase power, whose voltage varies proportionally with the engine's RPM. In order to allow either source of power to be supplied to the vehicle, a diode rectifier board was employed. The board is labeled "TSB5" on "SHEET 2 OF 2" of the AROD schematic in Appendix C. When external power was supplied, the diode circuit passed the DC power directly through, but the power supply was effectively isolated from return surge voltages by the diodes. With the engine running, the alternator's three-phase AC output is converted to 20-50V DC by the diode rectifier board. A 1000  $\mu$ f, 100V electrolytic power capacitor was used to smooth voltage ripples in the DC output.

The diode rectifier board is also equipped with a circuit which had been used by AROD as a path for charging the standby battery. This feature was not used in the Archytas configuration. A photo of the diode rectifier board is shown in Figure 10. A schematic of the diode rectifier board circuit is shown in Figure 11.

# 2. Signal Distribution Buses

All signal and electrical connections passing from forebody to rearbody were connected through distribution buses attached to the exterior of the chassis. A photo of

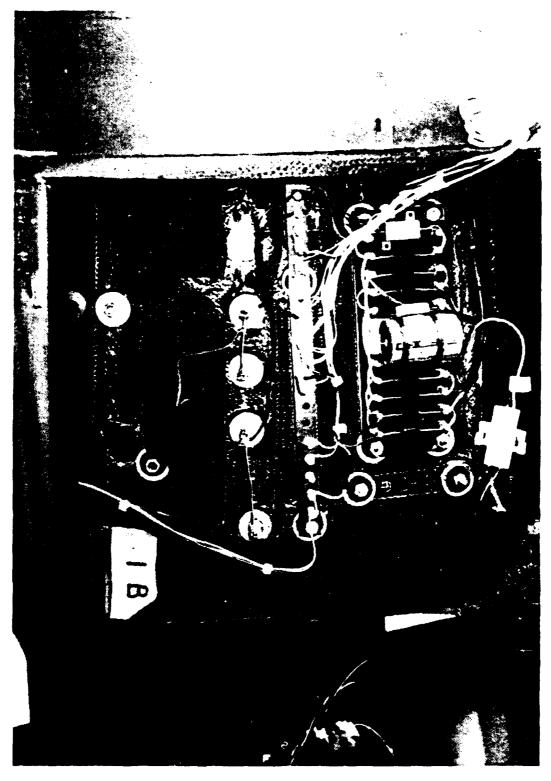


Figure 10 Photo of Chassis-mounted Diode Rectifier Board (TSB5)

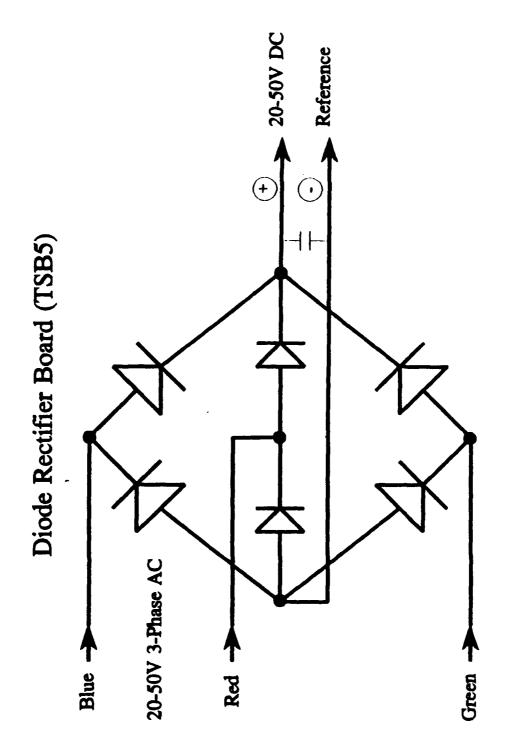


Figure 11 Schematic of Diode Rectifier Board (TSB5) with 3-Phase AC Input

a distribution bus is shown in Figure 12. The buses were labeled TSB# (for terminal strip board), where # indicates the nearest strut, i.e., TSB2 for the bus mounted next to strut two. The buses were useful for troubleshooting, as all important signals and voltages were accessible directly by simply removing the chassis outer shell, without requiring removal of forebody or rearbody hardware.

# 3. Engine

As previously mentioned, the Archytas UAV used a Herbrandson Dyad 280 2-cycle engine. The engine is supported by struts from the chassis interior to the rearbody, as well as by connecting rods running directly from the chassis interior to the engine. Engine starting was done with a detachable pull cord which wound around a starting pulley connected to the top of the engine drive shaft. A fuel mixture of 50:1 unleaded gasoline to oil was used. A safety checklist, shown in Appendix D, was developed for use during engine runs to ensure that proper safety precautions were followed.

# C. REARBODY

The rearbody was supported by four radial support struts which connected to the chassis' interior. The aerodynamic control vanes were attached to the lower portion of the rearbody. The umbilical cord was connected to the endcap at the bottom of the rearbody. The normal endcap designed for use at the end of the rearbody was replaced by a spare forebody topcap to facilitate secure mounting of the umbilical cord. Figure 13

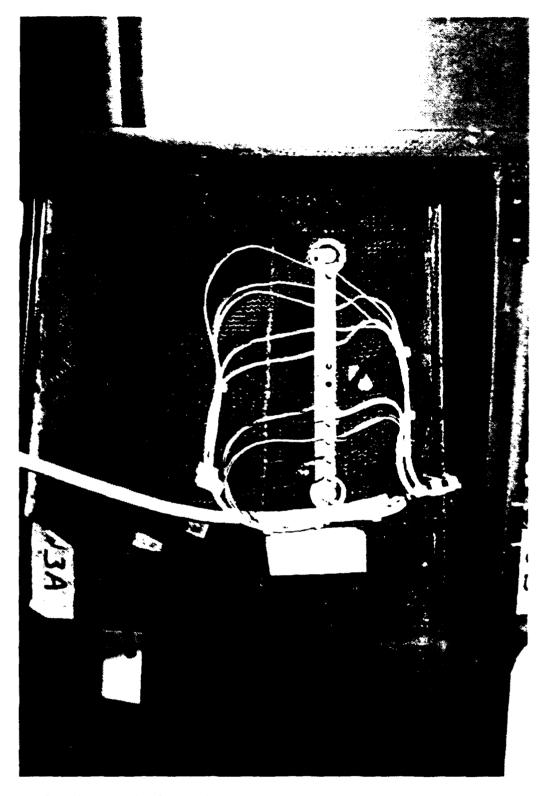


Figure 12 Photograph of Chassis Bus Board

shows the rearbody/chassis assembly, with the control vanes, servos, and umbilical connection visible in the foreground.

#### 1. Control Vane and Throttle Servos

Standard RC modeling servos were used to control aerodynamic vane positions and throttle position. The servos were driven by varying the time width of a 0-5 volt pulse from a minimum of 1.0 ms to a maximum of 2.0 ms, with a nominal midrange of 1.5 ms. PWM servo control is described in *Section d*. They were powered by +5V from a power regulator in the forebody. The term 'servo' is short for servomechanism or servomotor, which Webster defines as a relatively small, low-powered device to control a much larger control force [Ref. 13]. In this application, the servos were small electrically-driven motors used to control position of the aircraft's aerodynamic control vanes, which in turn created much larger aerodynamic forces and moments. Two different types of servos were used.

# a. Futaba Servos

Sandia engineers used Futaba FP-S34 quarter-scale RC modeling servos for AROD's control vanes. The FP-S34's are constructed of mostly plastic parts, including the drive-train gears. The throttle servo used was a Futaba FP-S31S. Sandia engineers chose to obtain servo positions for SAS feedback for AROD by simply tapping into the servos' internal single-wiper potentiometers [Ref. 8]. It was felt for the Archytas that such a method could cause interference with the internal feedback circuitry of the servo. The Futaba servos' lack of ruggedness, coupled with their already being

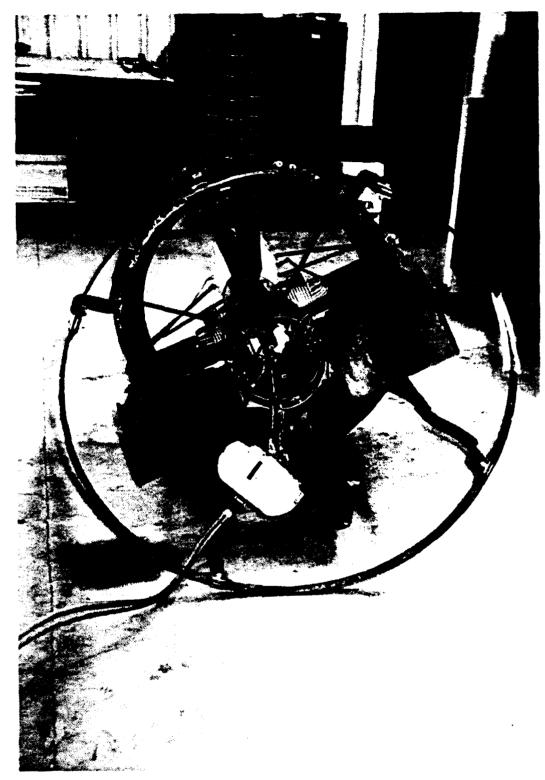


Figure 13 Photograph of Rearbody/Chassis Assembly

modified with position leads, led to a decision to use new servos for the Archytas control vanes.

# b. Condor Servos

The servos chosen to replace the AROD's Futaba control vane servos were MS-747WB servos from Condor RC, Inc. The new servos are constructed with metal gears, are generally more durable, and provide 50% more torque (167 oz-in for the Condor MS-747WB versus 112.6 oz-in for the Futaba FP-S34). The MS-747WB servos are modified with dual-wiper potentiometers to allow for non-interfering electrical connections for incoming signals and feedback vane position signals. The original AROD Futaba FP-S31S throttle servo was retained for the Archytas. Throttle position is deemed not necessary for control feedback at this phase of the project, so no signal is taken from the servo's potentiometer.

# c. Servo Modeling

A second-order Futaba FP-S34 servo model was developed by Merz [Ref. 2, p. 57] and Davis [Ref. 3, p. 116] by simulating a step input and observing the servo's response. Their model can be expressed as a second-order transfer function:

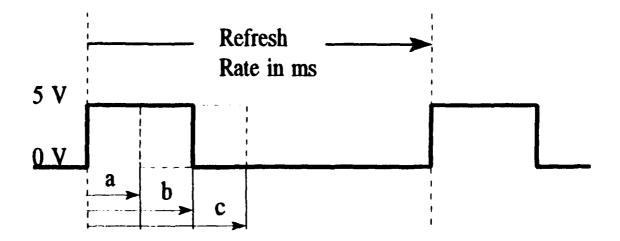
$$\frac{C(s)}{R(s)} = \frac{2745.8}{s^2 + 74.1s + 2745.8}$$
 (3)

where the values of  $\zeta=0.707$  and  $\omega_n=52.4$  rad/sec were determined experimentally. Similar tests will need to be done for the Condor MS-747WB servos.

# d. PWM Servo Control

known as pulse-width modulation (PWM). PWM signals are comprised of rectangular pulse trains. The width of each pulse corresponds to a position command for its respective servo. The signal can be sent to the aircraft in a variety of ways, such as through an umbilical cord, as in this phase of the project. In the umbilical method, each signal has its own dedicated wire. This method of transmission is called parallel data transmission. Alternatively, the PWM signal may be sent serially by an RF transmitter, using a method known as time-division multiplexing. The signal may also be generated aboard the vehicle, as will be required for autonomous flight. Each servo has a circuit which compares its incoming PWM signal to a reference signal corresponding to its current position. If there is a difference between the two, the servo's internal motor is turned to try to drive the difference, or error, to zero.

The nominal, or midrange, pulse width for general purpose RC servos is 1.5 milliseconds. Full servo movement in one direction is commanded by a pulse width maximum of 2.0 ms, while full movement in the opposite direction corresponds to a pulse width minimum of 1.0 ms. Figure 14 shows a typical PWM signal waveform with all parameters labeled. The "refresh rate" determines how frequently a new pulse is sent and/or received. Standard RC servos operate at a refresh rate of about 20 ms (a frequency of 50 Hz). [Ref. 16]



a: minimum pulse width - 1.0 ms

b: nominal, centered pulse width - 1.5 ms

c: maximum pulse width - 2.0 ms

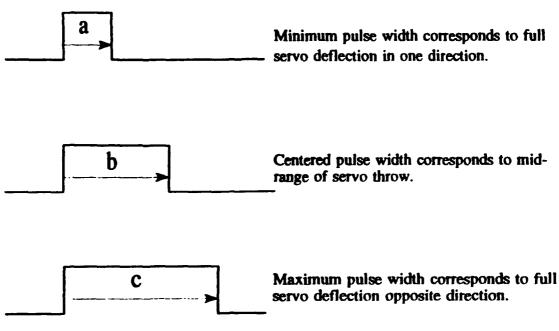


Figure 14 Typical PWM Waveform

# 2. Ignition and Tachometer

Engine ignition spark is provided to each cylinder's spark plug by the solid-state capacitor discharge ignition (CDI) system, which is housed in the rearbody below the alternator. A 28V DC-DC converter located in the forebody provided power to the CDI system. Also, a kill switch mounted on the control junction board is connected via the umbilical cord directly to the ignition leads. The connection is made on chassis bus board TSB2N

A tachometer signal is provided within the CDI circuitry which could be monitored during engine operation and displayed on an oscilloscope. The tachometer signal is generated by a magnetic pickup mounted adjacent to the engine drive shaft which is exposed to plate spinning with the shaft. The plate has notches in its perimeter which caused a spike in the pickup's magnetic field as the plate spun past the pickup, which in turn produces observable pulses on the oscilloscope. The gap from pulse to pulse represented ms per revolution, so the conversion from ms/rev to RPM is:

$$RPM = \left(\frac{ms}{rev} X \frac{sec}{1000 ms} X \frac{min}{60 sec}\right)^{-1}$$
 (4)

# 3. Alternator

The engine-driven 20-50V three-phase AC alternator is also mounted with the rearbody. It connected to the engine drive shaft through a bulkhead separating the rearbody from the chassis. The alternator's three-phase output harness is fed through the bulkhead and out through a support strut to the outer chassis. There it connected directly

to the diode rectifier board (TSB5) and was rectified to DC for distribution to the rest of the vehicle.

# D. VEHICLE INTERCONNECTIONS

Wiring harnesses connected the forebody to the chassis and the chassis to the rearbody. Surplus AROD harnesses were used wherever possible, as many of the connections required are the same as had been implemented on the AROD system. The only new connections not remaining from AROD were those involving the umbilical cord. The two AROD schematics, which can be found in Appendix C, detail many more connections than were used on Archytas at this phase of the project. Also included in Appendix C are schematics showing only those connections which were used in the Archytas application for this phase of the project. Table 5 defines the connections used for this thesis referenced to the AROD and Archytas schematics. Plug numbering was the same as for the AROD system in most cases, as all cables, plugs and sockets on surplus AROD equipment are well marked and labeled. Differences from the AROD system are noted in Table 5 as "new", or existing labels have an "N" suffix added to them. For instance, the new plug for strut three is labeled "P103N".

In summary, the three sections of the Archytas vehicle each contained hardware utilized in this thesis. The forebody contains the three sensors, two rate sensors and a vertical gyro, plus their associated signal conditioning circuits. The chassis is the mounting location for bus boards which connect signals and voltage supplies from

forebody to rearbody. The rearbody houses the alternator, CDI, and servos, and is the mounting location for the umbilical cord.

Most connecting wiring harnesses were derived from existing surplus AROD harnesses. New harnesses and bus boards, labeled with an "N", were built to connect the necessary signals and voltages to the umbilical cord. New plugs and bus boards which were developed for this work, P21N, P103N, TSB3N, and TSB2N, have pin diagrams enclosed in Appendix B. Their physical locations are described in Table 5.

# TABLE 5: FOREBODY/CHASSIS/REARBODY CONNECTIONS

Reference: AROD-3 SCHEMATIC, PAGES 1 & 2, Appendix B

# Forebody:

# **Intraconnections:**

Plug P1- VERTICAL GYRO to A1 CHANNEL CONDITIONING BOARD

Plug P2- RATE SENSORS to A1 CHANNEL CONDITIONING BOARD

Plug P5- Sensor Outputs and Servo Position Feedbacks (via P102) to

A1 CHANNEL CONDITIONING BOARD

Plug P6- A1 CHANNEL CONDITIONING BOARD to Filter Board and Power Supplies

Plug P13- Fan to 28V DC-DC CONVERTER PS7

Plug P17- FILTER BOARD to Exterior Plug P103N (N=new)

8 Sensor Outputs plus Common

#### **Exterior Connections:**

Plug P101- Strut 1 Harness to Chassis Bus Board TSB1

Plug P102- Strut 2 Harness to Chassis Bus Board TSB2

Plug P103N- Strut 3 Harness to New Chassis Bus Board TSB3N (N=new)

# Chassis:

All Chassis Connections were to/from Forebody & Rearbody via Bus Boards
TSB1, TSB2, TSB2N, & TSB3N described above and below with the
exception of direct connections (no plugs) from Diode Rectifier Board
TSB5 to TSB1 (20-50V DC). An additional plug was installed in the
harness from the AC alternator output to the diode rectifier board
(TSB5) for troubleshooting electrical system problems.

# Rearbody:

#### **Intraconnections:**

Five Servo Signal/Power and Feedback Plugs

Umbilical Inner Plug (New)

#### **Exterior Connections:**

Plug P200/P202- 5V/Common to Servos and 20-50V 3-Phase AC from Alternator directly to Diode Rectifier Board TSB5 (AC) and TSB1 (5V)

Plug P201- Signals to/from Servos and Electrical Power to CDI Ignition from Chassis Bus Boards TSB2 and TSB2N (N=new)

Plug P21N- New 21-pin Connector connect TSB2N and TSB3N to Internal
Umbilical 24-pin Cannon Plug Connector (N=new)

Umbilical Cord Exterior 24-pin Cannon Connector (New)

# V. CONTROL VANE GUIDANCE 'C' ROUTINES

Implementation of control laws to guide the Archytas UAV's control vanes was accomplished through 'C' routines which controlled the counter/timer and A/D boards. The evolutionary process which lead up to a routine for hovering flight began with several simpler intermediate steps. Each new generation of 'C' routine advanced the system to meet the next requirement. Initially, a system to generate a servo control signal using PWM was implemented. Then, a method of quickly and reliably commanding inputs was needed and a joystick was added. Next, a way of providing a simple feedback using a sensor aboard the vehicle in order to control the vehicle in one degree-of-freedom was needed, and a roll rate sensor with an uncoupled SISO controller was added. When the simple SISO feedback system was successfully implemented the system was advanced to three-axis control. Command inputs for three axes from dual joysticks were added. To accomplish hovering flight control, more sensory data was needed, so three-axis rate sensors and a vertical gyro were added to provide data for a MIMO controller. This introduction describes the current state of the Archytas control system.

As an introduction, the two routines written by Merz [Ref. 2, Appendices A and B] to generate PWM signals and control them through an A/D process are summarized in Section A. Then, Davis' reduced-order SISO controller for roll [Ref. 2, pp. 62-3] was incorporated via discretized equations. This was done in a routine called *roll.c*, which

is described in Section B. The routine is listed in Appendix A. The final routine,  $ai\_el\_ru.c$ , took input from two joysticks, mixed the commands, then sent separate, independent signals to each vane. This improvement allowed command of three aerodynamic surfaces, aileron, elevator, and rudder, through a varying combination of four control vanes. Section C of this chapter describes how  $ai\_el\_ru.c$  differs from previous routines, while the routine listing can be found in Appendix A.

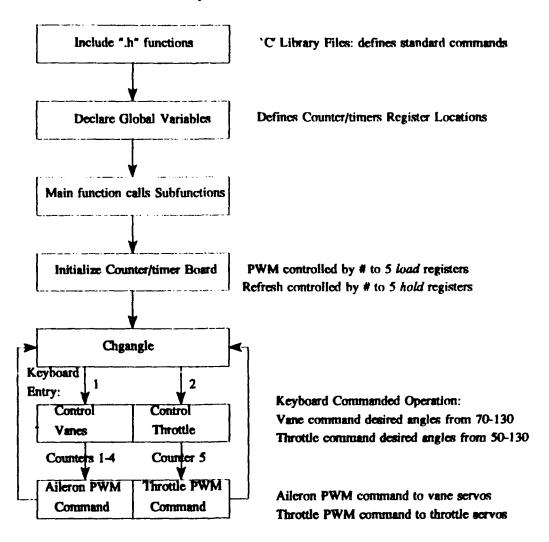
# A. PWM VANE CONTROL ROUTINES

PWM signal generation was initially accomplished by Merz with a routine called pwm.c, which used only the counter/timer board. Vane positions were selected by numeric inputs on the PC keyboard. Next, the A/D process was added in a routine called atod.c, which took inputs from a single joystick to control the vanes collectively, so that all four moved in the same direction. This was effectively commanding an aileron input.- Two routines, both written by Merz [Ref. 2], are summarized. Flow charts for both pwm.c and atod.c routines are shown in Figures 15 and 16.

# 1. Basic PWM Control Using Counter/timer Board

The first Merz routine, pwm.c, uses a counter/timer to generate the pulses of the PWM signal, controlling both the pulse width and refresh rate. This was done by loading numbers into the counter/timer's five counters' load and hold registers, respectively. The registers are storage locations within the counter/timer's circuitry which are the heart of the device's operation. The value entered into each counter's load register controls the pulse width, while the value entered into the hold register controls

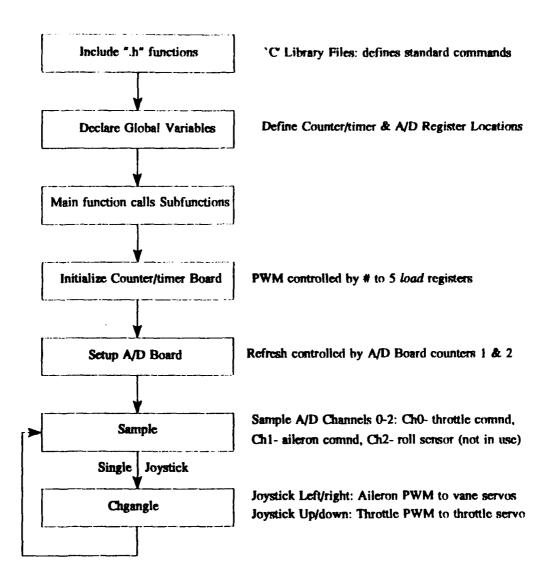
# pwm.c Flowchart



Reference: Quartz and CIO-CTR Counter/timer Manuals
(Written by Paul Merz, Ref. 2, Appendix A)

Figure 15 Flow Chart for pwm.c Routine

# atod.c. Flowchart



Reference: CIO-AD16jr and DAS-16 A/D Board Manuals

Reference: CIO-CTR and Quartz Board Manuals

(Written by Paul Merz, Ref. 2, Appendix B)

Figure 16 Flow Chart for atod.c Routine

the interval between pulses. Counters one through four control pulses sent to the four respective vanes, while counter five controls throttle pulses. Desired commands to the servos are entered by numerical input from a PC keyboard. Vane commands range from an "effective joystick angle" of 70 to 130, which represented a -30° to +30° vane deflection, a span of 60°. The term "effective joystick angle" is used since these vane command values represent actual angles of the stick with respect to horizontal in each degree-of-freedom. When the vanes are properly adjusted in the servo mounting brackets, a 100 keyboard command achieves a centered or 0° vane position. Throttle commands from the keyboard ranged from an effective joystick throttle angle of 50 at idle to 130 at maximum power, a total span of 80° on the actual joystick. [Ref. 2, Appendix A]

# 2. PWM Signal Control Via A/D Board

The use of effective joystick angles in pwm.c was in anticipation of the next generation Merz routine, atod.c, which added sampled actual joystick analog inputs through the A/D board. Besides the addition of joystick input, a basic difference from pwm.c was the use of the A/D board's counters to control the PWM refresh rate. The necessary refresh rate was calculated by Davis to be 10 ms [Ref. 3, p. 55]. This was implemented through trial and error input of values to the A/D counters until the proper refresh rate was achieved as observed on an oscilloscope display. The 12-bit A/D board measured analog inputs from 0 to 5 volts and translated them to a digital value between 0 and 4096 [Ref. 2, Appendix B]. The two degrees-of-freedom available from the joystick represented throttle inputs (up/down) and aileron commands (left/right).

# B. SISO ROLL CONTROLLER ROUTINE

Implementation of the reduced-order SISO roll controller model developed by Davis [Ref. 3, pp. 62-3] consisted of adding the discretized control equations to atod.c. Also, the necessary adjustments were made to sample another A/D channel for the roll rate sensor in addition to the two already being sampled for throttle and roll command. The sampled data were then scaled to a true scale by subtracting the digital value of the sensor's initial bias voltage. The resulting routine, roll.c., includes the equations developed by Davis [Ref. 3, pp. 62-3]. A flow chart of the routine is shown in Figure 17. Appendix A contains the routine listing for roll.c., including explanatory comments. Modifications to Merz' atod.c. routine which are included in roll.c. are:

- Within the "sample" function, control of the A/D conversion process is passed from external-triggering to software-triggering during the continuous loop to allow for the sequential sampling of multiple channels;
- The "sample" function includes discretized equations for the reduced-order control model, utilizing a lookup table to assign proper values to the vane servos;
- Software limiting is used in the "sample" function to prevent vane angles past 30°, which would result in vane stall, by assigning a constant vane command when the vanes reach 30°;
- A "senbias" function samples the roll rate sensor input 10 times, then averages, before initiation of routine loop; calculated average bias is then subtracted from every roll rate sample.

# C. MIXED CONTROL SURFACE GUIDANCE ROUTINE

The use of two joysticks is required for control of all three aerodynamic control surfaces and throttle. Commands to the four vanes needed to be mixed so that the three control functions could be achieved using the same four vanes. As previously mentioned

# roll.c Flowchart

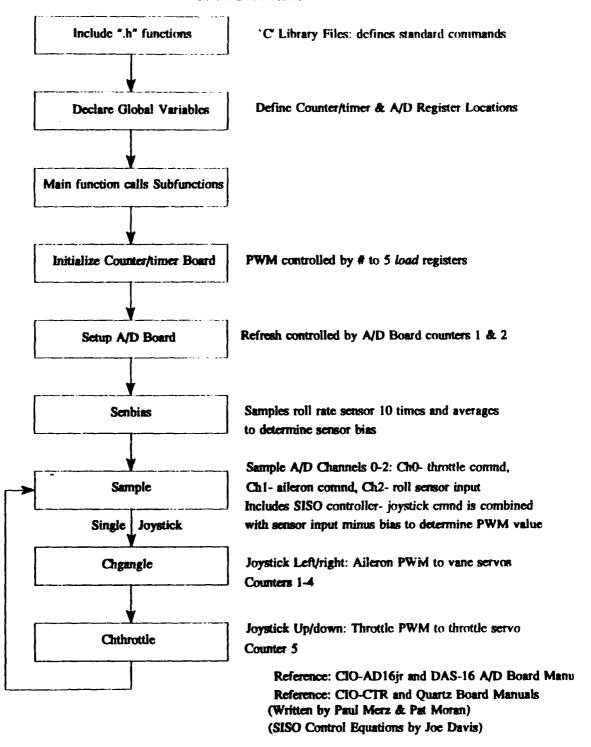


Figure 17 Flow Chart for roll.c Routine

in Chapter III, vanes one and three represent the upper and lower rudder, vanes two and four the left and right elevator, and a combination of all four vanes represent the aileron.

The equations to translate three joystick degrees-of-freedom into four vane commands is:

vane 
$$1 = \left(\frac{aileron}{4} + \frac{rudder}{2}\right)$$
  
vane  $2 = \left(\frac{aileron}{4} + \frac{elevator}{2}\right)$   
vane  $3 = \left(\frac{aileron}{4} - \frac{rudder}{2}\right)$   
vane  $4 = \left(\frac{aileron}{4} - \frac{elevator}{2}\right)$ 

where vanes 1-4 represent the values sent to the counter/timer to determine each vane servo's pulse width, and aileron, rudder, and elevator represent the commands input through each of the three respective joystick degrees-of-freedom. The factors of ½ and ¼ arise from the fact that elevator and rudder commands are split between two vanes each, while aileron commands must be split among all four vanes. This algorithm, developed by Kaltenberger [Ref. 14], accomplished the necessary mixing of aileron, elevator, and rudder inputs to allow four independent vane positions to be commanded. Several trial and error loops were required to determine proper multiplicative and additive factors to apply to the A/D digitized values to achieve the proper vane deflection angles for a given joystick input while keeping the vane servos properly aligned. The factors applied to each channel, from the sample function in ai\_el\_ru.c, are:

throttle = 
$$(\text{channel0} - 2714) \cdot 0.12 + 80$$
  
aileron =  $(\text{channel1} - 1600) \cdot 0.4 + 200$   
elevator =  $(\text{channel2} - 1850) \cdot 0.2 - 80$   
rudder =  $(\text{channel3} - 720) \cdot 0.2 - 80$ 

This system was implemented in  $ai_el_ru.c$  which is listed in Appendix A. A flow chart of  $ai_el_ru.c$  is shown in Figure 18. Improvements written into  $ai_el_ru.c$  over previous generation routines include:

- The use of 'C' language "prototypes" to pass variables from function to function, and to specify to the compiler what each function expects as inputs and outputs. [Ref. 18]
- Use of arrays to minimize repetition of similar sequences and improve speed. Arrays in 'C' are indexed starting at '0', which agreed with the A/D board's convention of numbering channels starting with Channel 0. [Ref. 18]
- Reduced sampling period from the 10 ms rate used in previous routines to 25 ms, in anticipation of additional computing time necessary for the more calculation-intensive MIMO control law implementation. This was done by increasing the numbers assigned to the A/D board's counters in the "setup" function, as commented in the routine listing in Appendix A.

In summary, two routines have been written to accomplish specific tasks which built upon each other. These routines were based upon PWM and A/D routines written by Merz [Ref. 2]. The first routine (roll.c) used PWM signals commanded from a single joystick and a SISO controller to control single-axis roll. The second is a routine (ai\_el\_ru.c) to command the PWM signal using dual joysticks to biend three joystick inputs into four independent vane commands.

# ai\_el\_ru.c Flowchart Include ".h" functions 'C' Library Files: defines standard commands Define Counter/timer & A/D Register Locations Declare Global Variables Prototype each function's input & output for compiler **Prototypes** Main function calls Subfunctions PWM controlled by # to 5 load registers Initialize Counter/timer Board Refresh controlled by A/D Board counters 1 & 2 Setup A/D Board Refresh rate reduced to 25 ms from previous 10 ms Sample A/D Channels 0-3: Ch0- throttle, Sample Ch1- aileron, Ch2- elevator, Ch3- rudder Arrays used to index channels through A/D loop Dual **Joysticks** Left Joystick Right Joystick Left/Right rudder ailcron Chgangle Up/Down throttle elevator

Reference: CIO-AD16jr and DAS-16 A/D Board Manuals
Reference: CIO-CTR and Quartz Board Manuals
(Written by Pat Moran, Basic PWM & A/D Sampli ng by Paul Merz)

Figure 18 Flow Chart for ai\_el\_ru.c Routine

# VI. SYSTEM EVALUATION AND RESULTS

As with any developmental system, evaluation of the system is not complete until the design has been validated by appropriate testing. This chapter details the tests done to evaluate the Archytas UAV control system algorithms, vehicle, and ground control hardware designs. Results are presented for each phase of work. Conclusions drawn from the results and recommendations for improving the system based on the results are presented in the following chapter.

# A. VEHICLE AND GROUND CONTROL EQUIPMENT EVALUATION

The hardware results of this thesis fall into the categories of ground control and vehicle equipment. Ground Control Equipment development is described in Chapter III, while On-board Vehicle Equipment design is discussed in Chapter IV. Evaluation of the hardware in each area is described in the following sections.

# 1. Ground Control Equipment

The design of ground control equipment consisted of expanding the existing system to accommodate the additional requirements of the MIMO controller, which required more signal capacity through the umbilical cord for the additional sensors. A new umbilical cord was required and was added to the control junction board with an additional connection bus. The control junction board was modified to provide for dual joystick pilot command, and to allow for remote connection of the external DC power supply. The A/D board connections were expanded to allow for the sampling of the 12

channels necessary for MIMO control. When all wiring connections were accomplished, each signal line was checked to ensure electrical continuity. The joysticks were verified to work over their full range, as given in Table 3 in Chapter III. A new 486 PC was configured and used for generation of routines and lab testing of control systems, providing dual-site PC operating capability.

The counter/timer and A/D boards were tested using software provided from their manufacturers which exercised their circuitry to determine proper installation in the PC. Their operation was also validated by observation of the expected PWM outputs from PWM routines when displayed on an oscilloscope display.

# 2. On-board Vehicle Equipment

Accomplishments in the area of On-board Vehicle Equipment consisted of reconfiguration of the sensor pod from its previous rearbody location to the AROD-style forebody location. Surplus AROD harnesses were installed for the dual-axis rate sensor, the vertical gyroscope, and three-axis accelerometers (although not yet used in this phase of the project). The wiring of new A1 Channel Conditioning Board, Filter Board and signal output plugs was also completed.

The chassis was reconfigured using AROD bus boards and new umbilical chassis bus boards. The AC power supply diode rectifier board was relocated from the sensor pod to the chassis exterior. New wiring harnesses to and from the umbilical were constructed. The rearbody AROD servo wiring harness was used in its entirety. Additional rearbody umbilical-related harnesses and plugs were constructed.

Again, when all vehicle wiring connections had been accomplished, all signal wires were checked for proper electrical continuity from the vehicle to the control junction board through the umbilical cord. The use of chassis bus boards, which were accessible easily on the exterior of the chassis, greatly improved the ease with which signal flow could be traced.

While conducting engine runs to determine vehicle characteristics, the +5V voltage regulators mounted in the forebody which supply 5V servo power were tested. On two occasions, the 5V regulators burned out. It is believed that high engine RPM during testing drove a higher than expected voltage output from the alternator, which in turn supplied too high a voltage to the regulator. Since excess voltage to the regulator is dissipated through heat discharge, if more voltage is supplied than can be dissipated, the regulator fails. A temporary fix was installed, which consisted of adding resistors in series with the input to the +5V regulator to reduce the voltage potential being supplied to the regulator. The resistors also failed later during engine runs. Subsequent testing was done only on external power. Recommendations in Chapter IV include resolving this problem.

The alternator and ignition systems were tested during engine runs. The vehicle operated normally when the external DC power supply was removed, indicating proper operation of the alternator. No specific observations were made of the three-phase output from the AC alternator, such as voltage for a given engine RPM. The ignition system operated adequately for engine operation, but was also not specifically observed. The CDI tachometer output was used to determine engine RPM, as given by

Equation 4 in Chapter IV. Tachometer speeds were verified using a Futaba transmitter's RPM meter, which measured engine RPM through a photo-optic sensor which sensed propeller speed.

Severe vehicle vibration was apparent during all engine operations with the sensor pod mounted in the rearbody position, causing numerous component failures. The engine was not run after the sensor pod was relocated to the forebody position. To prevent future vibration-related problems, rubber grommets were used in hardware connections whenever possible to dampen vibrations. All electrical connections were converted to sturdy plugs held together by nuts and bolts to prevent vibration-induced disconnections.

The sensor outputs from the vertical gyroscope and two rate sensors were tested in the lab using Personal VISSIM, Version 2, a software program capable of displaying analog signals in real time through another A/D board mounted in the 486 computer. Digitized graphs of the analog sensor outputs, resembling oscilloscope displays, were used to simultaneously observe all five sensor outputs at once. Simulated vehicle movements were achieved by holding the forebody sensor pod independent of the vehicle and moving it in the desired direction. Both vertical angle signals varied linearly with the angle of inclination of the sensor pod as it was inclined in each axis. Roll, pitch, and yaw rate signals varied in magnitude with the rate at which the pod was rotated in each respective axis. Sensor outputs through the forebody A1 Channel Conditioning and Filter Boards were verified by observing the proper 0-5V ranges from each sensor output with a voltmeter.

The servos which guide the control vanes and throttle were observed to exhibit noticeable chatter when operated at the 10 ms refresh rate used by Merz and Davis. Reducing the refresh rate to 25 ms reduced the chatter significantly. The use of commercially available noise suppressors also helped minimize chatter. The noise suppressors operate through an 74LS04N hex inverter microchip, which inverts each PWM signal, then reinverts the signal. The original PWM signal is retained, but noise is suppressed by the double inversion process.

#### B. CONTROL ROUTINE EVALUATION

Evaluation of the PWM and A/D vane control system consisted of commanding inputs to the control vanes via joysticks and observing the deflection responses of the vanes and throttle to ensure that they remained within the vane stall limits of 30° for the vanes and within the engine throttle linkage limit for the throttle. This was done first with a single joystick, commanding throttle and ailerons, then with dual joysticks commanding throttle, ailerons, elevator, and rudder. SISO control law implementations were tested by simulating vehicle motions, thereby upsetting the installed sensors, and observing the vane counteraction responses. MIMO control laws were written into a routine, with testing accomplished and documented by Kaltenberger [Ref. 14].

Merz' basic PWM and A/D conversion programs [Ref. 2] were tested in the lab on a stationary vehicle. The next step in complexity, implemented in roll.c, involved addition of the SISO roll control. This led to testing with the engine running. A roll disturbance was simulated by upsetting the vehicle with a rolling force. The vane

deflection response observations were purely qualitative in that the vanes responded in the proper direction to counter the disturbance. However, no analysis was conducted to determine if the amount of vane deflection was appropriate for the given disturbance.

Implementation of the dual joystick vane control program,  $ai_el_ru.c$ , was again tested primarily in the lab. Evaluation involved observation of vane deflection responses from commanded inputs from both joysticks. Again, the vane deflections and the throttle linkage remained within linear limits.

#### 1. Implementation of SISO Controller

The simplified roll rate controller developed by Davis [Ref. 3] was tested with the vehicle engine running while mounted in a rigid test stand. The system satisfactorily commanded the proper direction aileron vane deflections to counteract simulated roll disturbances, although the tests were only qualitative and not fully conclusive since no analytical data on the degree of deflection for a given disturbance could be obtained due to the limits of the test stand. Joystick roll command was operable allowing input of a slow operator-commanded rolling motions within the limits of the test stand (about one full turn of the vehicle).

A problem which arose during testing of the roll controller implementation was the inability of the controller to recognize when the vanes had reached their aerodynamic effective limit of 30° deflection. A simple solution was added which stopped the aileron vane deflection when it reached 30°, which is the limit of deflection where stall is expected to occur, even if the joystick continued to command an increasing roll. This was done by freezing the PWM command to the vanes when the limit was

reached. The controller, however, continued to integrate the difference between the joystick command and the roll rate sensor input, thereby calculating an increasingly greater deflection command. The effect is known as controller windup. It can be resolved by adding an antiwindup algorithm to the controller to limit the controller from calculating commands larger than the limit of the device it is controlling. [Ref. 19]

## 2. Dual Joystick Control Vane Guidance

Control of the four vanes using mixed input from dual joysticks was accomplished. Vane deflections were controllable from both joysticks throughout the  $\pm 30^{\circ}$  range of each vane, providing a full range of control for each aerodynamic control surface. The equations used to implement vane mixing and the factors applied to each digitized channel are provided in Equations (5) and (6) in Chapter V. Slowing the refresh rate from 10 ms to 25 ms in  $ai_el_ru.c$  improved the steadiness of the servo responses. Through discussions with a Futaba representative, it was determined that the faster refresh rate was driving the servos faster than they were designed to react. Futaba FP-S34 servos are designed to operate on a 20 ms refresh rate. [Ref. 15]

#### C. SUMMARY OF RESULTS

Ground control equipment was expanded from the previous design to allow for the greater signal capacity required by the MIMO controller. The vehicle was redesigned to a forebody-mounted sensor pod, due primarily to excessive vibration observed during engine runs. Center of gravity requirements for forward flight stability also were improved by moving the pod forward. Voltage regulators failed during engine runs,

which resulted in loss of power to the servos. A temporary solution was installed, but it too was ineffective.

Implementation of the SISO controller was accomplished and tested in conjunction with the work of Merz and Davis. The SISO controller reached a limit when the vanes were commanded by joystick to their maximum angle, which resulted in vane position being fixed at that position until the controller was commanded back into its region of authority. This problem may be rectified by adding an *antiwindup* algorithm to the controller, as discussed previously.

Dual joystick-commanded control of three aerodynamic control surfaces through four control vanes was achieved. Array indexing and 'C' language prototypes were implemented to improve software efficiency. Servo chatter was reduced by slowing the PWM signal refresh rate from 10 ms to 25 ms.

#### VII. CONCLUSIONS AND RECOMMENDATIONS

This thesis began under the assumption that the rear-mounted sensor pod design previously implemented by Merz [Ref. 2] would be suitable for the Archytas UAV. However, it became obvious, after several engine runs during testing to acquire aerodynamic data about the vehicle, that the vibration generated by the vehicle's two-stroke engine was more than the rear-mounted sensor pod design could withstand. This, combined with further stability calculations by Stoney [Ref. 9], led to a decision to move the sensor pod to the forebody location. The focus of this thesis became reconfiguration of the ground control equipment including the umbilical cord and associated control junction board, and redesign of the vehicle to a forebody-mounted sensor pod.

Much awareness was gained in the area of real-world electrical system design. The impact of such things as floating grounds, noise interference, and signal crosstalk became apparent when they impacted system design and testing. What at first seemed to be a simple requirement to "move the pod forward", became a much larger requirement to resurrect surplus AROD harnesses and reverse-engineer the AROD system design. A frequent question became "Why did they do that?", which usually lead to more research through old AROD documentation and schematics. Also, unexpected airframe design considerations impacted the design of several electrical components involved with this thesis, such as wiring harness paths which had to be moved to allow for mounting of the wing spars.

An "antiwindup" scheme should be added within the controller equation loop, which would prevent the integral controller from continuing to integrate the errors after the 30° vane software limit has been reached. This would prevent the controller from continuing to calculate corrections after the vanes had reached their aerodynamic stall limits. [Ref. 19]

All equipment should be mounted using vibration damping techniques and fixtures.

Before actual flight test of the vehicle, all hardware should be secured using LokTite, or a similar product which will prevent screws and nuts from backing off due to vibration.

The three-phase output of the AC alternator should be measured during engine operation to determine AC voltage ranges and volts/RPM. Investigation should be done toward the replacement of the AC alternator system with DC battery power, which could lead to a reduction in vehicle weight, as suggested by Hoffman [Ref. 17]. A full power budget will be required to determine complete present and future system power requirements.

The forebody +5V DC power regulators which supply servo power should be redesigned so that they will not fail when the engine runs at high speeds. This problem could be rectified by replacement of the engine-driven AC alternator with DC batteries.

The hovering flight MIMO control equations developed by Kaltenberger [Ref. 14] were formatted for computer implementation. The MIMO program design needs to be completed, then tested using a simulated model of the Archytas UAV fed by feedback of model outputs.

#### APPENDIX A: 'C' PROGRAMS DEVELOPED FOR THE ARCHYTAS UAV

```
/***********/
/* ROLL.C */
/***********/
                                                                                             */
/* Written by Paul Merz & Pat Moran, Oct 92
/* Uses Counter/timer & A/D boards
                                                                                             */
                                                                                             */
/* Implements Roll rate control using Joe Davis' simplified controller.
/* Input comes from joystick or roll sensor to guide 4 vanes together.
                                                                                             */
/* Throttle controlled from joystick. PM 4/27/93
                                                                                             */
#include < dos.h >
#include < stdio.h >
#include < time.h >
                              /* Ctr/timer board, base address
                                                                                     */
int datreg = 544;
int conreg = 545;
                                /* Ctr/timer board, base addr +1
                                                                                     */
int basaddr=768; /* A/D board, base address int basep11=769; /* A/D board, base addr +1
                                                                                    */
                                                                                     */
                                /* A/D board, base addr +2
int mux = 770:
                                                                                    */
int statreg=776; /* A/D board, base addr +8
int intcont=777; /* A/D board, base addr +9
int pclock=778; /* A/D board, base addr +10
int inrange=779; /* A/D board, base addr +11
int cntr1=781; /* A/D board, base addr +13
int cntr2=782; /* A/D board, base addr +14
                                                                                    */
                                                                                    */
                                                                                    */
                                                                                    */
                                                                                    */
                                                                                    */
int cntrcon=783:
                                /* A/D board, base addr +15
                                                                                    */
float ts = .010;
float ki = .004376:
                                 /* # controls rate of response to stick mvmnt, */
float k1 = -.2027;
                                 /* .004376 gives slow roll, .04 gives quick roll */
int lookup[70], newangle, throttle, bias; /* Global variables
main()
initialize();
setup();
throttle = 51;
chthrottle():
```

```
senbias();
sample();
initialize()
                           /* This function is thoroughly commented
                           by Merz [Ref. 2, Appendix A]
int cmnd,i;
cmnd = 255;
                           /* reset all board functions
                                                              */
outportb(conreg,cmnd);
cmnd = 23;
                           /* select master mode register
outportb(conreg,cmnd);
cmnd = 176;
                           /* low byte enables fout
                                                              */
outportb(datreg,cmnd);
cmnd = 65:
outportb(datreg,cmnd);
cmnd = 249:
outportb(conreg,cmnd);
for (i=1;i<=5;i++)
  cmnd=i;
                           /* Select group 1-5
                                                                     */
  outportb(conreg,cmnd);
  cmnd=2;
                           /* Low byte set ctr 1-5 mode in CMR
  outportb(datreg,cmnd);
  cmnd = 27;
                           /* High byte: no gating for ctr 1-5
                                                                     */
  outportb(datreg,cmnd);
for (i=25; i < =29; i++)
  cmnd=i;
                           /* Load hold register for refresh rate
                                                                    */
  outportb(conreg,cmnd);
  cmnd=0;
  outportb(datreg,cmnd);
  cmnd = 10:
 outportb(datreg,cmnd);
for (i=9; i < =13; i++)
 {
 cmnd=i;
                           /* Select load register for pulse width
 outportb(conreg,cmnd);
 cmnd=110;
                           /* Load low byte into load register
                                                                    */
  outportb(datreg,cmnd);
 cmnd = 5;
```

```
outportb(datreg,cmnd);
for (i=233; i < =237; i++)
  cmnd=i;
  outportb(conreg,cmnd);
                          /* Load and arm ctr 1-5
                                                                   */
cmnd = 127;
outportb(conreg,cmnd);
for (i=1; i < 70; i++) lookup[i] = 50+i;
printf("COMPLETED INITIALIZATION OF SERVOS\n");
                          /* This function is thoroughly commented
setup()
                          by Merz [Ref. 2, Appendix A]
 int cmnd;
 cmnd=0;
                          /* Set the mux to read channels 0-2
                                                                          */
 outportb(mux,cmnd);
                          /* Set up pacer clock for clock driven-sampling
                                                                          */
 cmnd=2;
 outportb(pclock,cmnd);
                          /* Set up for correct input & type of voltage
                                                                          */
 cmnd = 5;
 outportb(inrange,cmnd);
                          /* This selects control reg for mode & ctr 1
                                                                          */
 cmnd = 118;
 outportb(cntrcon,cmnd);
                                                                          */
                          /* Load up counter one of pacer clock
 cmnd = 100;
 outportb(cntrl,cmnd);
 cmnd=0:
 outportb(cntrl,cmnd);
 cmnd = 182;
                          /* Selects control reg for mode & ctr 2
                                                                          */
 outportb(cntrcon,cmnd);
                                                                          */
 cmnd = 100;
                          /* Load ctr 2 of pacer clock
 outportb(cntr2,cmnd);
 cmnd=0;
 outportb(cntr2,cmnd);
 printf("COMPLETED CARD INITIALIZATION\n");
```

```
senbias()
                           /* This function samples the roll rate sensor
                           input 10 times to determine an average sensor
                           bias prior to entering the A/D sampling loop.
                                                                            */
  int x,lsb,msb,tsb,sreg;
                                                                             */
   x=0; bias=0;
                           /* Set counter and initial bias to 0
                           /* Count 10 times through bias loop
                                                                             */
   while (x < = 10)
   sreg=inportb(statreg); /* Check int bit of status reg for high
                                                                            */
                                                                             */
                           /* indicating start of new refresh cycle.
    sreg = sreg & lo;
   if (sreg = = 16)
                                                                             */
     lsb=inportb(basaddr);
                                    /* Read in lobyte
                                                                             */
     msb=inportb(basep11);
                                    /* Read in hibyte
     lsb=lsb >> 4;
                                   /* Roll lower 4 bits right
                                                                             */
     msb = msb < < 4;
                                   /* Roll upper 4 bits left
                                                                             */
                                   /* Bitwise 'OR'
                                                                            */
     tsb = msb \mid lsb;
                                                                            */
     bias=tsb + bias;
                                    /* Maintain cumulative sum
                                    /* Increment loop counter
                                                                            */
     x=x+1;
                                                                            */
   bias = bias / x;
                         /* Average bias by # of times thru loop
   bias = 2048 - bias:
                         /* 2048 is center of A/D range, so bias
                                                                            */
   printf(" bias = %d \n", bias); /* will be above or below center
                                                                            */
  printf("hit any key to start: "); /* Wait for operator start
                                                                            */
   scanf(" %d",&x);
                           /* Read in start (any #)
                                                                            */
sample()
 int x,cmnd,lsb,lsb1,lsb2,lsb3,sreg;
 float x1k,epink1,epink,epk,u1k,pk;
   x=1; epink = 0; /* Set loop counter to 0; Set initial values
                                                                            */
  pk=0;
                           /* of error increment & roll increment =0
                                                                            */
   while (x==1)
                           /* Infinite loop
                                                                            */
                                                                            */
   sreg=inportb(statreg); /* Check int bit of status reg for hi, indicating
   sreg=sreg & 16;
                           /* pacer clock has triggered new refresh cycle
                                                                            */
   if (sreg = 16)
                           /* When int=hi, enter A/D loop
                                                                            */
                           /* Set mux to count Ch 0-2
     cmnd=32;
                                                                            */
     outportb(mux,cmnd);
     cmnd = 0;
```

```
outportb(intcont,cmnd);
                                  /* SOFTWARE TRIGGERED A/D ONLY */
     outportb(basaddr,cmnd);
                                  /* IMMEDIATE A/D CONVERSION
     lsb = inportb(basaddr);
                                  /* Read in lobyte, MSB-8 to LSB+CHAN */
     lsb1 = inportb(basepl1);
                                  /* Read in hibyte
                                                                        */
                                                                        */
     lsb = lsb >> 4;
                                  /* Roll lower 4 bits right
                                                                        */
     lsb1 = lsb1 < < 4:
                                  /* Roll upper 4 bits left
     lsb1 = lsb1 \mid lsb;
                                  /* Bitwise 'OR'
                                                                        */
                                  /* IMMEDIATE A/D CONVERSION
     outportb(basaddr,cmnd);
     lsb = inportb(basaddr);
                                  /* READ LOW BYTE, MSB-8 TO LSB+CHAN*/
                                  /* READ HIGH BYTE, MSB TO MSB-7 */
     lsb2 = inportb(basepl1);
     lsb = lsb >> 4;
                                  /* ROLL LSBYTE RIGHT 4 BITS
                                                                        */
                                                                        */
     lsb2 = lsb2 < < 4:
                                  /* ROLL MSBYTE LEFT 4 BITS
                                                                        */
     lsb2 = lsb2 \mid lsb;
                                  /* 'OR' TO GET 12 BIT INFO
     outportb(basaddr,cmnd);
                                  /* IMMEDIATE A/D CONVERSION */
     lsb = inportb(basaddr);
                                  /* READ LOW BYTE, MSB-8 TO LSB+CHAN*/
     lsb3 = inportb(basepl1);
                                  /* READ HIGH BYTE, MSB TO MSB-7 */
     lsb = lsb >> 4:
                                  /* ROLL LSBYTE RIGHT 4 BITS
                                                                        */
     lsb3 = lsb3 < < 4;
                                  /* ROLL MSBYTE LEFT 4 BITS
                                                                        */
                                                                        */
     lsb3 = lsb3 \mid lsb:
                                  /* 'OR' TO GET 12 BIT INFO
     cmnd=2;
                                  /* RTN PACER CLCK-DRIVEN SAMPLING*/
     outportb(intcont,cmnd);
     cmnd = inportb(statreg);
                                  /* Read status reg, then write back
                                                                       */
     outportb(statreg,cmnd);
                                  /* to status reg- causes flip-flop reset
     x1k = ((lsb1 + bias)-2048) * .048828; /* Convrsion fm dig to rad of 'pcommand'*/
     pk = (1sb3-1033) * .0351;
                                  /* Read in current value of 'p' or roll rate */
     epk = pk - x1k;
                                  /* Tracking Error = 'p' - 'pcommand'
     epink1 = epink + epk;
                                  /* Sum cumulative errors
     u1k = ((-1 * ki) * epink1) - (k1 * x1k); /* Davis' Eqn. 4.19, Ref. 3, p. 63 */
     epink = epink1;
                                  /* Increment Error term
     newangle=(int)(u1k + 35);
                                  /* Truncate command, add normalizing factor */
     if (newangle < 1) newangle = 1;
                                             /* Software limit to keep total */
     if (newangle > 69) newangle = 69;
                                             /* vane deflect below +/-30 deg */
     newangle=lookup[newangle]; /* Keeps vane deflctn in +/-30 range
                                                                       */
     chgangle();
                                  /* Call change angle routine
     throttle = (int)((lsb2-2714) * .15 + 65);
                                             /* Throttle cmnd normalizing */
     chthrottle();
                                  /* Call change throttle routine
chgangle() /* CHANGES VANES VIA CONTROL EQNS, CALLED FM sample()*/
int i, hibyte, lobyte, angle, cmnd;
```

```
angle=((1900/206)*newangle+600); /* Algorithm to convrt fm deg to dig # */
                          /* Hibyte formed, residue remaining for lobyte */
   hibyte = (angle/256);
   lobyte = (angle-hibyte*256); /* Lobyte fromed from remaining residue */
                           /* Disarms ctrs 1-4 for loading
   cmnd = 207;
                                                                     */
   outportb(conreg,cmnd); /* Sends to control reg
   for (i = 9; i < 12; i++) /* Selects load reg for pulse width of ctrs 1-4 */
                           /* Loads lobyte, then hibyte into each ctr */
     outportb(conreg,i);
     outportb(datreg,lobyte);
     outportb(datreg, hibyte);
   for (i = 233; i < = 236; i++) outportb(conreg,i); /* Sets toggle high for ctr 1-4 */
                           /* Load and arm ctrs 1-4
   cmnd = 111:
   outportb(conreg,cmnd);
}
                     /* CHANGES THROTTLE, CALLED FROM sample() */
chthrottle()
 int i, hibyte, lobyte, angle, cmnd; /* This routine is same as CHGANGLE */
 cmnd = 208:
                           /* except it changes only ctr 5
                                                              */
                                                              */
 outportb(conreg,cmnd);
                          /* for throttle
 angle = ((1900/206)*throttle + 600);
 hibyte = (angle/256);
 lobyte = (angle-hibyte*256);
 cmnd = 13;
 outportb(conreg,cmnd);
 outportb(datreg,lobyte);
 outportb(datreg, hibyte);
 cmnd = 237;
 outportb(conreg,cmnd);
 cmnd = 112;
 outportb(conreg,cmnd);
}
```

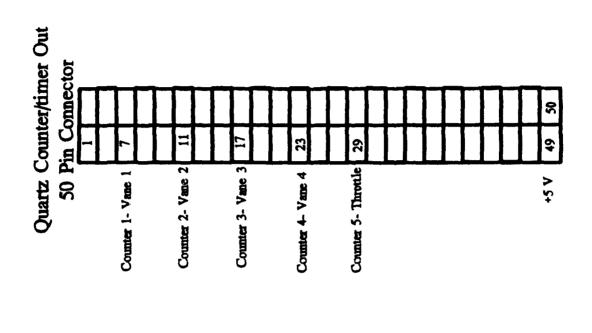
```
/* AI_EL_RU.C, ARCHYTAS UAV PROJECT, NPS MONTEREY */
| ***********************
                                          5/14/93
/* Written by LCDR Pat Moran
/* Basic PWM routine by LT Paul Merz [Ref. 2, App A & B]
                                                                   */
/* Demo to move aileron, rudder, elevator, & throttle
                                                                   */
/* from 2 joysticks. Blends 3 degrees-of-freedom into
                                                                   */
/* 4 independent vane commands.
                                                                   */
                                                                   */
/* With assistance from Chris Miller NPS EE Dept: Isaac
                                                                   */
/* Kaminer/Burke Kaltenberger for mixing algorithm.
#include < dos.h>
#include < stdio.h >
                                                                   */
#include < conio.h >
                          /* Only used by 'clrscr' command
int datreg = 544;
                          /* Ctr/timer board, base address
                                                                   */
                                                                   */
int conreg = 545:
                          /* Ctr/timer board, base addr +1
int basaddr=768;
                          /* A/D board, base address
                                                                   */
int basep11 = 769;
                                                                   */
                         /* A/D board, base addr +1
                          /* A/D board, base addr +2
                                                                   */
int mux = 770;
int statreg = 776; /* A/D board, base addr +8
                                                                   */
                        /* A/D board, base addr +9
                                                                   */
int intcont = 777;
int pclock=778; /* A/D board, base addr +10 int inrange=779; /* A/D board, base addr +11 /* A/D board base addr +13
                        /* A/D board, base addr +10
                                                                   */
                                                                   */
                         /* A/D board, base addr +13
                                                                   */
int cntr1 = 781;
                          /* A/D board, base addr +14
int cntr2 = 782:
                                                                   */
int cntrcon=783;
                          /* A/D board, base addr +15
                                                                   */
                                                                   */
void main(void);
                    /* Prototypes:
void initialize(void); /* prototyping functions- tells compiler
                                                                   */
void setup(void);
                    /* what goes into and out of each function
                                                                   */
                    /* i.e. int sample(void) means integer input
                                                                   */
void sample(void);
void chgangle(int,int,int,int); /* and no or 'void' output
                                                                   */
void main(void)
  initialize();
                    /* Main function calls all others
                                                                   */
  setup();
  sample();
```

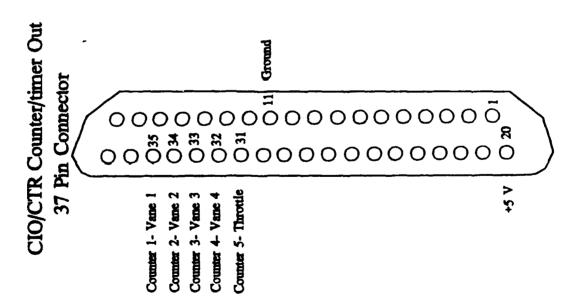
```
void initialize(void) /* Initilizes cntr/timer board
                                                              */
  int i:
                     /* Clears screen for display
  clrscr();
                                                              */
  outportb(conreg,255); /* Reset all board functions
                                                              */
  outportb(conreg,23); /* Select master mode register
                                                              */
  outportb(datreg, 176); /* Lobyte enables FOUT, F1 source
                                                              */
  outportb(datreg,65); /* Hibyte selects binary division
                                                              */
                     /* disable incr, 8bit bus, FOUT on
                                                              */
                     /* divide by 1.
                                                              */
  outportb(conreg, 249); /* Disable prefetch for write ops
                                                              */
  for (i=1;i<=5;i++)
    outportb(conreg,i); /* Select ctrs 1-5
                                                              */
    outportb(datreg,2); /* Lobyte: set modes ctrs 1-5 in CMR */
    outportb(datreg,27);/* Hibyte: no gating for ctrs 1-5
                                                              */
  for (i=25; i < =29; i++)
    outportb(conreg,i); /* Load hold registers for refresh rate
   outportb(datreg,0); /* Ctr1 reg+Ctr2 reg= Refresh Rate
   outportb(datreg, 10);/* This combo = 25 ms rate (40 Hz)
  for (i=9; i < =13; i++)
   outportb(conreg,i); /* Select load registers for pulse width */
   outportb(datreg, 110); /* Sets time for next pulse
                                                              */
   outportb(datreg,5);
  for (i=233; i < =237; i++) outportb(conreg,i);
  outportb(conreg, 127); /* Load & arm ctrs 1-5
                                                              */
  printf("\n\n\n *** COMPLETED COUNTER/TIMER INITIALIZATION ***\n");
void setup(void)
                 /* Initializes A/D board
                                                              */
   outportb(mux,48); /* Sets mux to read chan 0-3
   outportb(intcont,2); /* Sets pacer for clock-driven sampling */
   outportb(inrange,5); /* Sets for input voltage range: 0-5V
                                                              */
           /* Fm data sheet: SC-01, RW-11, M-011, BCD-0 */
   outportb(cntrcon, 118);/* Ctr 1, READ/WR lsbyte 1ST, mode 3, binary */
   outportb(cntr1,100); /* Load ctr 1 lower byte
                                                              */
   outportb(cntr1,0); /* Load ctr 1 upper byte
                                                              */
```

```
/* Fm data sheet: SC-10, RW-11, M-011, BCD-0
   outportb(cntrcon, 182); /* Ctr 2, READ/WR lsbyte 1ST, mode 3, binary */
   outportb(cntr2,250); /* Load ctr 2 lower byte
                                                                     */
   outportb(cntr2,0); /* Load ctr 2 upper byte
                                                                     */
           /* Ctrs 1&2 combo sets sampling period: 25ms
                                                                     */
             *** COMPLETED A/D BOARD INITIALIZATION ***\n");
void sample(void)
                     /* Samples channels via A/D board
                                                                     */
   int x,i,cmnd,lsb,msb,sreg,elev,rud,ail,thr,ch[4];
     x=1:
     while (x = 1) /* Endless loop
                                                                     */
      sreg=inportb(statreg);/* Read stat reg to check introt bit
                                                                     */
                           /* 'AND' with 16 to get only 5th bit
      sreg = sreg \& 16;
                                                                     */
                           /* If intrpt (5th) hi, pulse revd
      if (sreg = 16)
                                                                     */
                                                                     */
    outportb(mux,48);
                           /* Read Channels 0-3
    outportb(intcont,0);
                           /* Software triggered A/D only
                                                                     */
    for (i=0; i < =3; i++) /* Read Ch0-3 for thr, ail, elev, rud
                                                                     */
    outportb(basaddr,0);
                           /* Immediate A/D conversion
                                                                     */
    lsb = inportb(basaddr); /* Read lobyte, MSB-8 to LSB + Chan
                                                                    */
    msb = inportb(basep11); /* Read hibyte, MSB to MSB-7
                                                                     */
    lsb = lsb >> 4;
                           /* Roll lsbyte right 4 bits
                                                                     */
    msb = msb < < 4;
                          /* Roll msbyte left 4 bits
                                                                     */
    ch[i] = msb \mid lsb;
                           /* 'OR' to get 12 bit info
                                                                     */
    outportb(intcont,2);
                          /* Return to pacer clock-driven sampling
                                                                    */
   cmnd=inportb(statreg); /* Read status reg, then write back to
                                                                     */
   outportb(statreg,cmnd); /* status reg: resets Pin25 flip-flop
                                                                     */
   thr = (int)((ch[0]-2714)*.12+80); /* Input fm throttle up/down
                                                                    */
   ail = (int)((ch[1]-1600)*.4+200); /* Input fm aileron lt/rt
                                                                     */
   elev = (int)((ch[2]-1850)*.2-80); /* Input fm elevator up/down */
    rud = (int)((ch[3]-720)*.2-80); /* Input fm rudder lt/rt
                                                                     */
   chgangle(thr, ail, elev, rud);
                                    /* Call chgangle function
                                                                     */
}
```

```
void chgangle(thr, ail, elev, rud) /* Uses counter/timer to calculate
                                                                       */
                                     /* each pulse width
 int i, hibyte, lobyte, angle, vane[5];
                                     /* V1; Translation algorithm fm 3 */
      vane[0] = ail/4 + rud/2;
                                     /* V2; control surfaces to 4 vanes */
      vane[1] = ail/4 + elev/2;
                                                                       */
      vane[2] = ail/4 - rud/2;
                                     /* V3;
                                                                       */
                                     /* V4:
      vane[3] = ail/4 - elev/2;
                                                                       */
                                     /* Throttle needs no conversion
      vane[4] = thr;
                                                                       */
                                     /* Disarm counters 1-5
      outportb(conreg,223);
      for (i=0; i < =4; i++)
       {
    angle=((1900/206)*(vane[i])+600); /* Convert fm deg to dig #
                                                                       */
                                     /* Calc high byte, residue left
                                                                       */
    hibyte = (angle/256);
                                                                       */
                                     /* Calc low byte fm residue
    lobyte = (angle-hibyte*256);
                                      /* Load counters 1-5
                                                                       */
    outportb(conreg,(i+9));
                                                                       */
                                      /* Load low byte
    outportb(datreg,lobyte);
                                                                       */
                                      /* Load high byte
    outportb(datreg, hibyte);
                                                        /* Set toggle hi 1-5 */
      for (i=233; i < =237; i++) outportb(conreg,i);
                                      /* Load & arm counters 1-5
                                                                       */
       outportb(conreg, 127);
}
```

#### APPENDIX B: HARDWARE SPECIFICATIONS

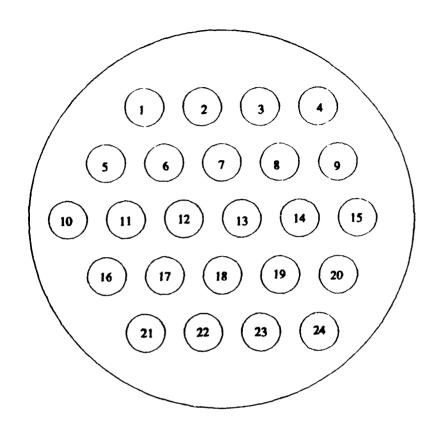




Counter/timer Board Pin-out Connections

## CIO-16ADjr or DAS-16 Note: Same Pin-out for either 19 Low Level Signal Ground 16 Ch 10 Elevator Position 15 Ch 11 Rudder Position 17 Ch 9 Aileron Position 18 Ch 8 Yaw Angle Ch 12 Not Used Ch 13 Not Used Ch 14 Not Used Ch 15 Not Used Analog-to-Digital (A/D) In 7 Ground 37 Pin Connector 14 12 00000000000000 00000000000000000 Throttle Ch 0 37 Roll Command Ch 1 36 Pitch Command Ch 2 35 Roll Rate Ch 4 33 Yaw Command Ch 3 34 Pitch Rate Ch 5 32 Yaw Rate Ch 6 31 8 Trigger 25 Counter 2 Out 20 Pitch Angle Ch 7 Umbilical Cord All others from From Joysticks

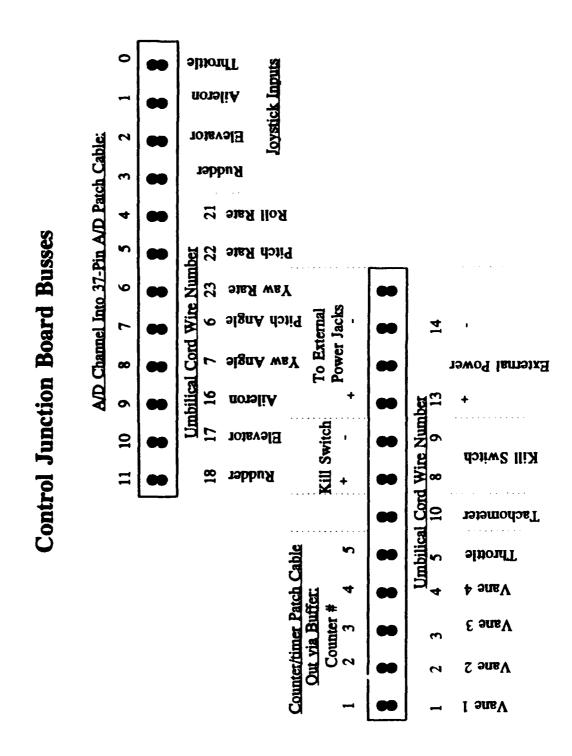
A/D Board Pin-out Connections



- 1. Vane 1 Signal
- 2. Vane 2 Signal
- 3. Vane 3 Signal
- 4. Vane 4 Signal
- 5. Throttle Signal
- 6. Pitch Angle
- 7. Yaw Angle
- 8. Kill Switch (+)
- 9. Kill Switch (-)
- 10. Tachometer
- 11. Vacant
- 12. Vacant

- 13. 28 V External Power (+)
- 14. 28 V External Power (-)
- 15. Chassis Ground (Shield)
- 16. Aileron Position Feedback
- 17. Elevator Position Feedback
- 18. Rudder Position Feedback
- 19. Vacant
- 20. Vacant
- 21. Roll Rate Sensor (p)
- 22. Pitch Rate Sensor (q)
- 23. Yaw Rate Sensor (r)
- 24. Common

**Umbilical Cord Cannon Plug Pin-numbering Convention** 



**Umbilical Cord Connections at Control Junction Board** 

1. SANDARA TAN TO THE COLLEGE TO THE SANDAR TO THE TAN TO THE SANDAR TO THE TAN TO THE TAN THE	The March Street	TACH TACH THE TACH TH	10 Tests 2 Tes	52344 - / f et 34 v ridoeft (200) 1 0% kurt (00 fect (200)		APE 11 STATE AND AND COLD ACTOR OF SOURCE TO SUBSTITUTE AND COLD ACTOR OF SOURCE AND COLD ACTOR	The state of the s
	THE STATE OF THE S	0;	2705 ATTENDED TO THE TOTAL			NA SO MA	SUTBLY SC TH S. TH

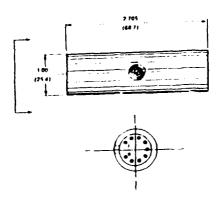
Roll Rate Sensor (RT-01) Wiring Connections



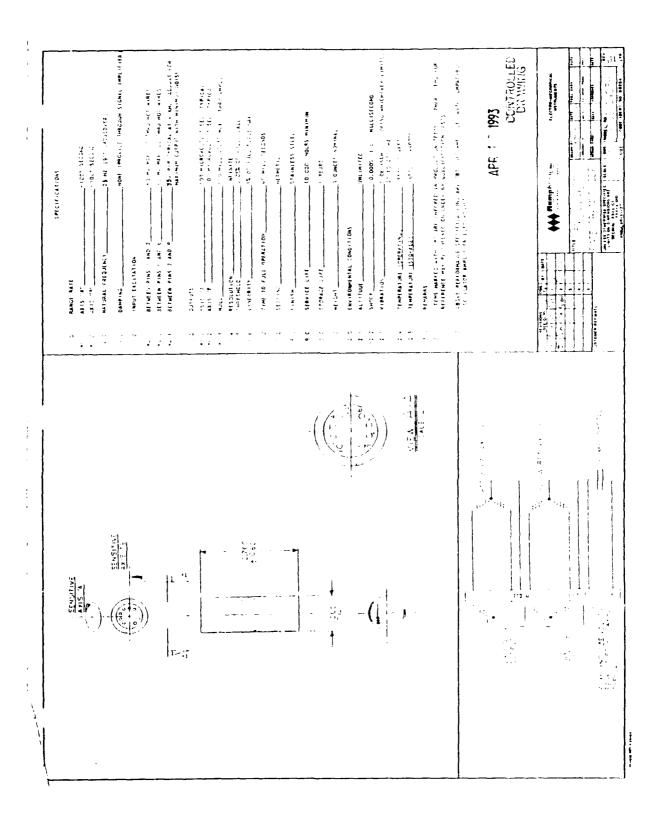
RT01-0504-1
Sungle-axis sensor ruis typities the name series It is a cylinder one incline diameter and its length is inversely proportional to the rate range. It is hermotically sealed, weighs only three ounces, and is controlled by an optional numature electronic package.

SPECIFICATIONS

<b>0. 20</b>	
Rate range	· 250 /second maximum
Natural frequency	25 Hz menmum
Damping	,0.7 typical
Output	· 25 millivolts nominal
Linearity	<ul> <li>this of full scale.</li> </ul>
Resolution	Infinite
Hysteresis	. None
Null signal	· 5 /second
Threshold	Virtually zero
Noise	0 1% of full scale
Sensor excitation	3 6 volts at 60 MA DC
	18 votis peak at 4K Hz
Pump drive	Hulimitad
Altitude	10 000 G
Shock	
Temperature	55 to 185 C
Life	5 years
Weight	3 ounces



**Humphrey RT-01 Single Rate Sensor Specifications** 



Pitch & Yaw Rate Sensor (RT-09) Wiring Connections

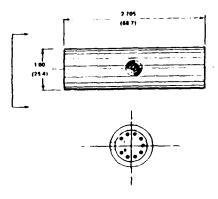


RT09-0103-1

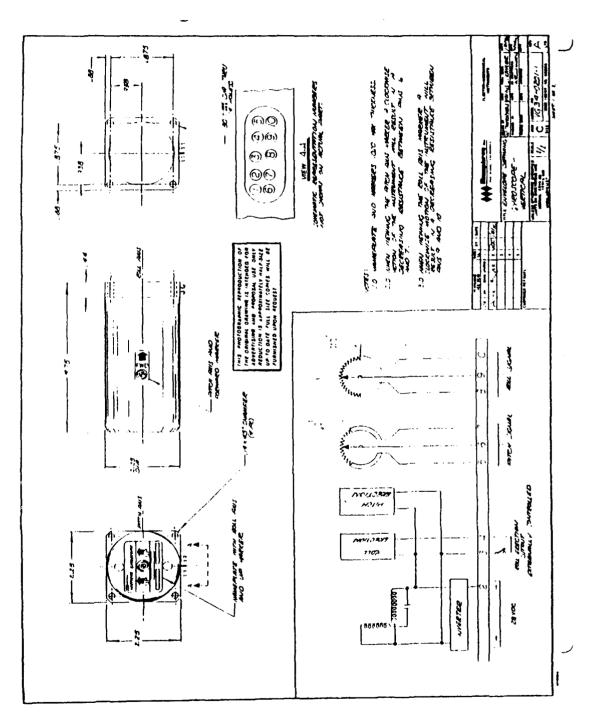
Dual axis sensor. The RT09 series incorporates two sets of sensor wires, orthogonally mounted, in one package to give dual axis operation. ation. Externally, units are physically identical with the RT01 series and operation is essen

## SPECIFICATIONS

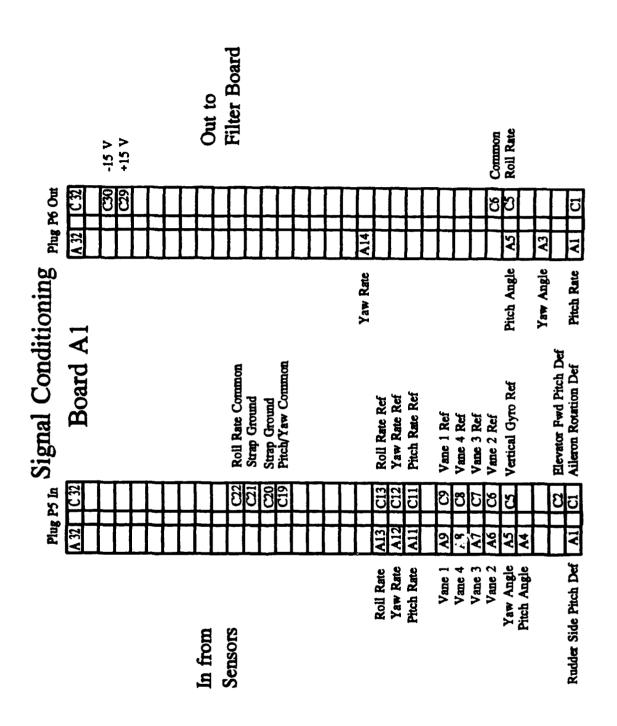
Range rate	100%/second each axis
Natural frequency	25 Hz mioimum
Damping	0.7 typical
Output	· 10 millivolts nominal
Linearity	<ul> <li>1% of full scale</li> </ul>
Resolution	Infinite
Hysteresis	Noue
Null signal	·? sec
Threshold	Virtually zero
Noise	. O 1% of full scale
Sensor excitation	60 MA DC current thru
<b>5</b>	two pairs of wires
Pump drive .	.18 volts peak at 4K Hz
Attitude	Unlimited
Shock	10,000 G
Temperature	55 to +85 C
Life	5 years
Weight	



**Humphrey RT-09 Dual Rate Sensor Specifications** 

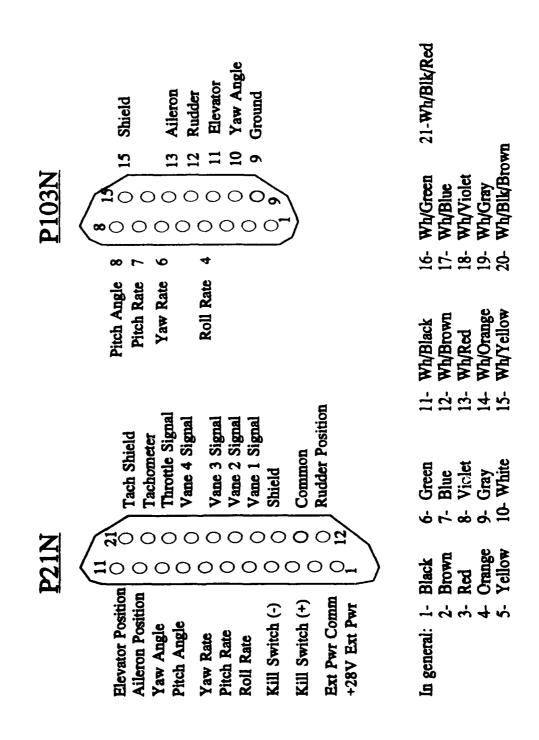


Vertical Gyroscope (VG-34) Wiring Connections



**Forebody A1 Channel Conditioning Board Connections** 

**Forebody Filter Board Connections** 



New Plug P21N and P103N Pin-out Diagrams

TSB3N	P103N In	4 Poli Rate 5	7 Pitch Rate 6	6 9 Yaw Rate 7	8 Pitch Angle 8	10 S Yaw Angle 9	13 • Aileron Position 10	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	12 Rudder Position 12	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	15 Shield 14	Wh/Black 16- Wh/Green 21-Wh/Blk/Red Wh/Brown 17- Wh/Blue Wh/Red 18- Wh/Violet
TSB2N	P201 Out	14	17	18	15	16		To TSB1 - 15	20,21	To TSB5 - 11	To TSB5 - 9	6- Green 11- 7- Blue 12- 8- Violet 13-
TS										Power	US Som	Black Brown Red
	đ	Vane 1	Vane 2	Vane 3	Vane 4	Throttle	Tachometer	Kill (+)	Kill (-)	+28V Ext Po	Ext Power Com	In general: 1- 1 2- 1 3- 1
	PZIN In	15	16	17	18	19	20	m	4	+	7	ក្ ក

New Chassis Bus Boards TSB2N and TSB3N Diagrams

# Futaba

# FP-S134 FP-534

For J. M. SG. MAGNUM, and CHALLENGER Series

For E. F. G. H. L. Series

## Large high torque watertight and dustproof servos

On the error of the error of them have the  $\rho_{\rm th}$ and the probability of the contract of the con regioner i tippo torque

- to the standard control of the control of the control of the property of the transfer of the control of the con
- A three modern of stages of participates smoother properties on a stage transfer and a specific record from the contract of  $(m, 2, \dots, n)$  , then first probability is a substitution of
- e field a temperature for for 10 projeto shipto tribing trapped was a contract and social Albert to a bandity
- $\bullet \ \ \mathsf{botourptip} \ \ \mathsf{constant} \ \mathsf{co$ me have the etrong and meable rate to place to a
- $\epsilon$  extrange performance in the new processing section of the contrast process of  $\epsilon$ control not artually no for thick-
- · Opening participal type prominers inches you a mounting year and have a excellent to be suspen-· tter t
- \* the identifible confined horns are as include

puter width control

Characteristic

Roster Type one of the control

Lever supply

Lever consequation

Output torpie

Operating speed

Dimensions

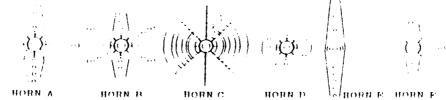
O232 111 + 0 2 700 10471

#### (PPROAUTIONS)

- 🖪 How the 2014 with Entare t M 20 MACHULE and CHAITERIQUE Colors for second The SSA with P. E. G. H. and T. Serier sets. There server cannot be good such other 1211 ......
- Install the geries es that the red adjuster flevible sire hinger and other parts speciate smoothly. Be represent a vietal when the travel is faces
- When coming derews install the dervo Chrongh i tophing and tighter the solice see that the buching is clightly engaled. If the genere, we too tight the engineeing our ort of the bushing will be just
- 🚍 A space serve herm is provide Use it as needed

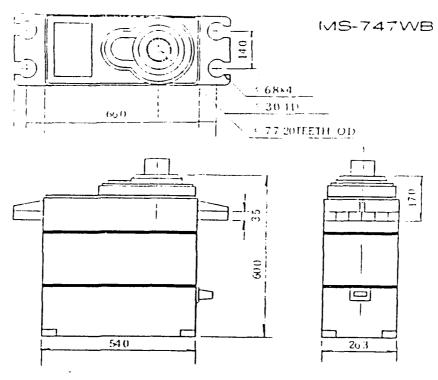
(Rittings, and specifications are subject to change without notice)

#### SPLINED SERVO HORNS



**Futaba FP-S34 Quarter-scale Servo Specifications** 





#### **SPECIFICATIONS**

SIZE: 2+ x3+ x1+ (54.0x60.0x26.3mm)

WEIGHT: 3.9 0Z (110 g)

SPEED: 0.26 SEC/60 DEGREES TORQUE: 167 OZ/IN (12 KG/CM) INPUT VOLTAGE: 4.8-6.0 V DC

INPUT SIGNAL: CONVENTIONAL R/C POSITIVE PULSE

WIDTH MODULATION WITH 1.5MS = 0.5

US NEUTRAL

CURRENT CONSUMPTION: IDLE .....25MA

NO LOAD ..... 300MA

MAX. .....1400MA

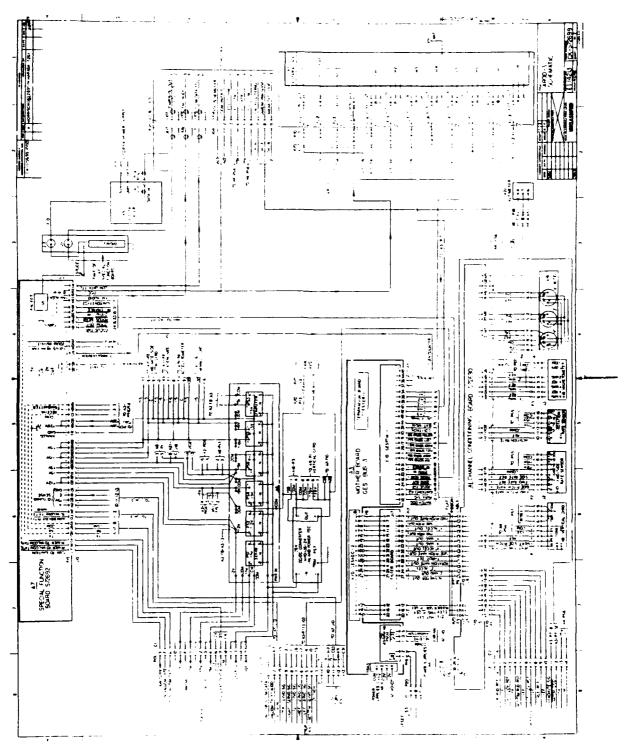
OUTPUT ROTATION: 90 & 180 DEGREES, TWO TYPES FEATUPES: MS-747WB SERVO HAS A HEAVY DUTY OUTPUT SHAFT WITH BALL BEARING AND

O-RING SEAL RUGGED EXTRA-THICK GEARS, ARM AND DISK, EXCELLENT

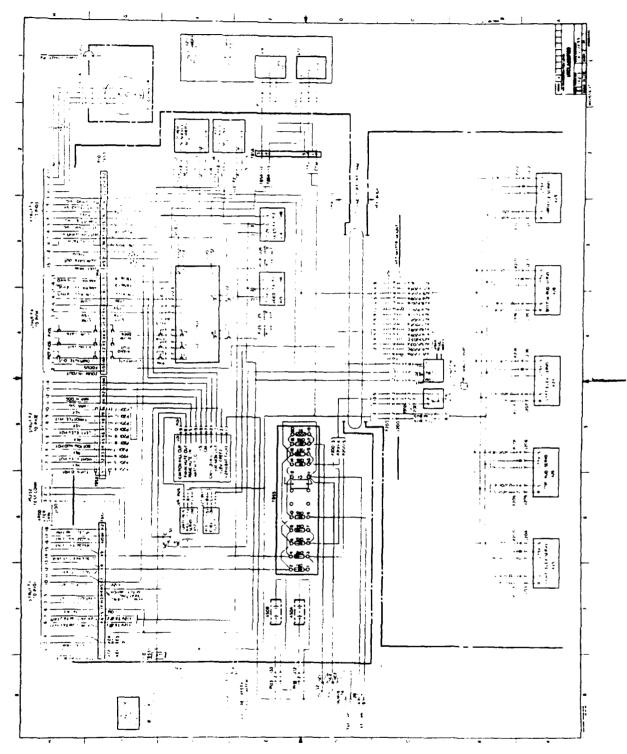
MECHANICAL AND ELECTRONICS QUALITY.

## **Condor RC MS-747WB Servo Specifications**

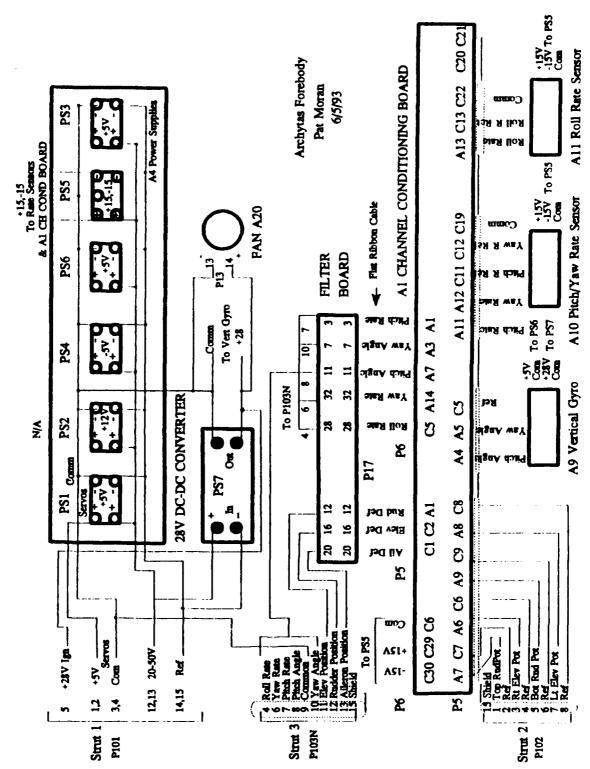
# APPENDIX C: AROD SCHEMATICS- AROD-3 FOREBODY & SHEET 2 OF 2



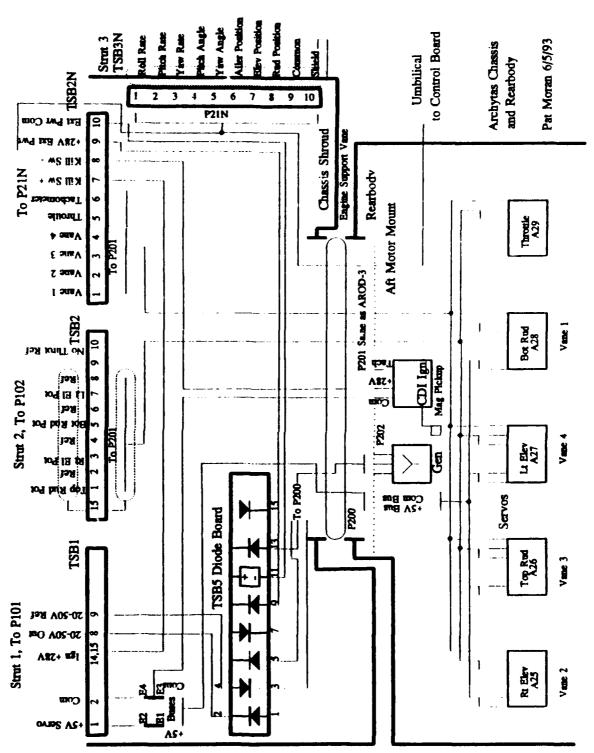
AROD-3 Forebody Schematic, Sheet 1 of 2



AROD-3 Forebody Schematic, Sheet 2 of 2



Archytas Forebody Schematic, Sheet 1 of 2



Archytas Forebody Schematic, Sheet 2 of 2

#### APPENDIX D: ARCHYTAS UAV ENGINE RUN CHECKLIST

#### ARCHYTAS ENGINE RUN CHECKLIST 1/22/93 PJM

Setup Keys for Bldg 249 Vehicle/stand to Bldg 249 (at least 2 people if pushing, or pull w/truck) Computer setup Oscilliscope w/probes & cord Voltmeter w/probes (runs on battery if desired) External power supply 28 VDC Tools- allen wrenches, screw drivers, etc Gasoline- premixed w/oil Fuel pump (pressurize gas can to feed fuel) Extension cords (1 already at Bldg 249, bring 1) Video camera w/film Ear protection: Foamies/MickeyMouse ears Safety glasses Electrical tape Paper towels Soldering Gun & solder Other items:

Engine Runs

Notify Prof Howard (X2870) of run/times (phone in Bldg 230- Don has key) Run area swept clean

Engine stand jacks raised

Personnel rqrd: computer operator (kill switch during start) brakeman (set brake with tie wrap if no roll rqrd) starter (assume kill switch duty when engine started)

All duties clearly-defined, primary operator designated Hand signals understood by all (due to noise) Hearing & eye protection

Computer up & running

General vehicle security walkaround- no loose panels, wires, etc. Fire extinguisher nearby (hanging outside entrance to Bldg 249) Golf course clear

Camera running

Extra observers well forward of vehicle

#### <u>Duties</u>

Computer operator- throttle, joystick, kill switch (primary operator) Brakeman- stand by brake, (may be omitted if no roll rqrd)
Starter- pull start cord, (man kill switch once engine started) Camera operator- safety obsrvr, stand by fire ext (omit w/camera on auto) MINIMUM PERSONNEL: 2 (camera running, no roll rqrd)

Reminder: KEYS FOR BLDG 249

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